

76978317440US

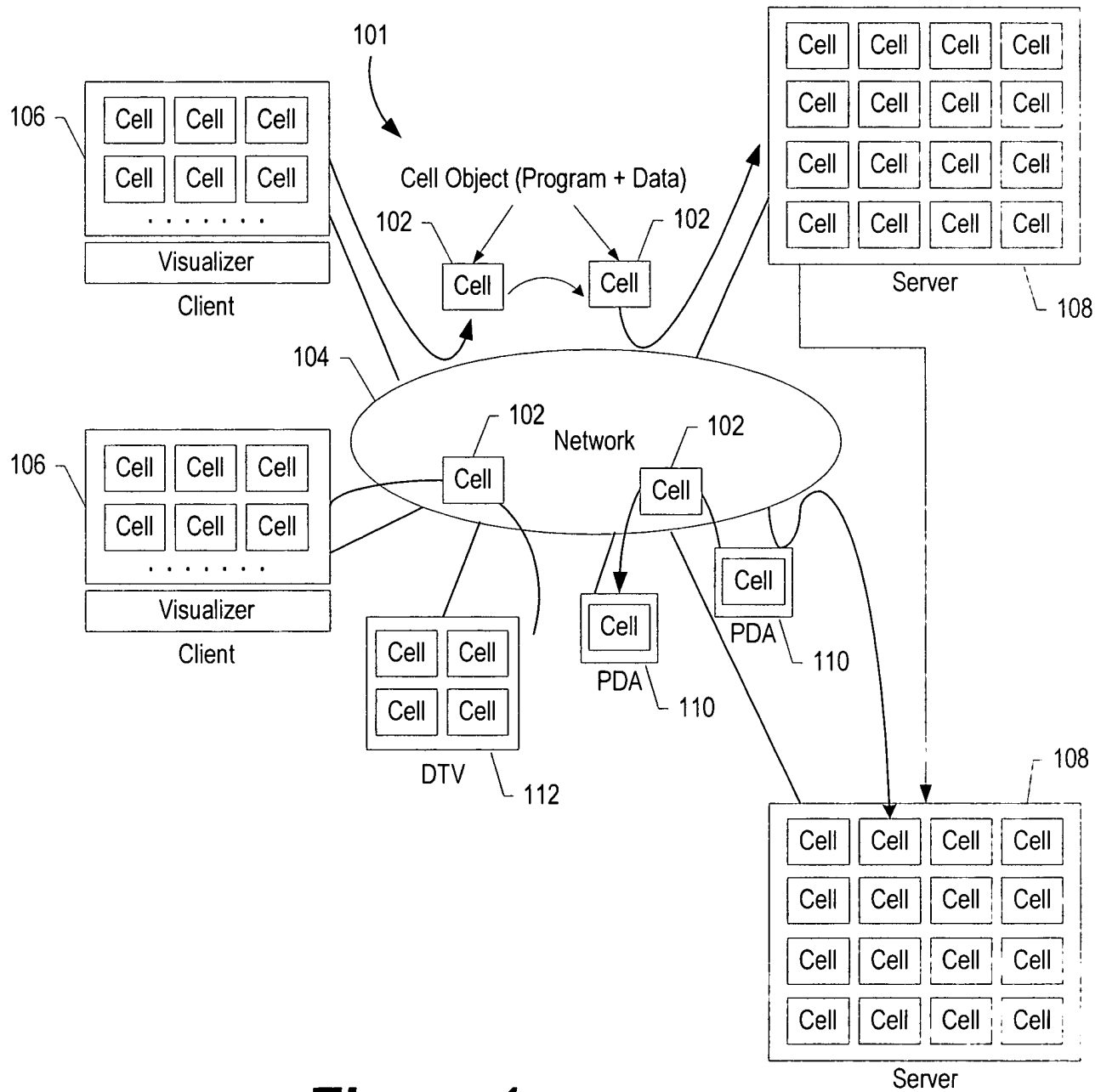
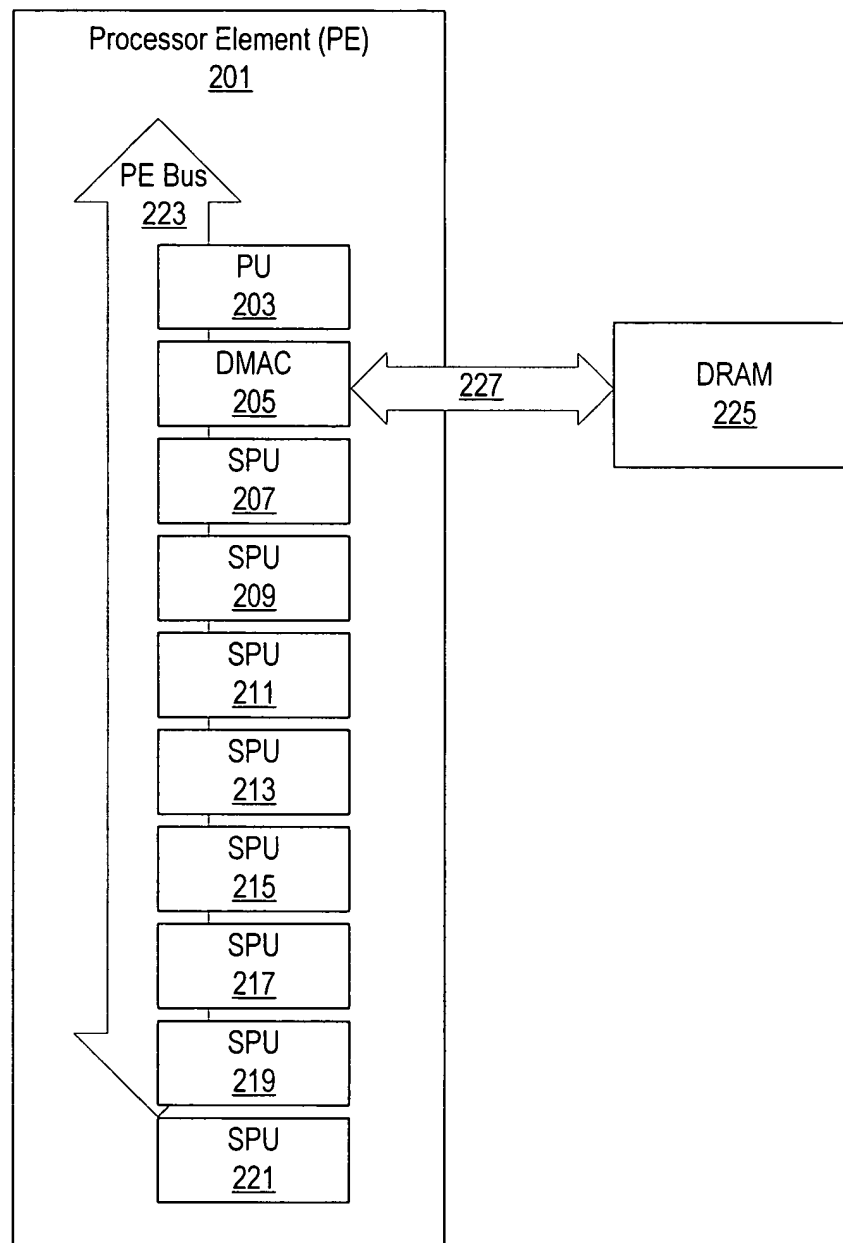


Figure 1

**Figure 2**

3 / 50

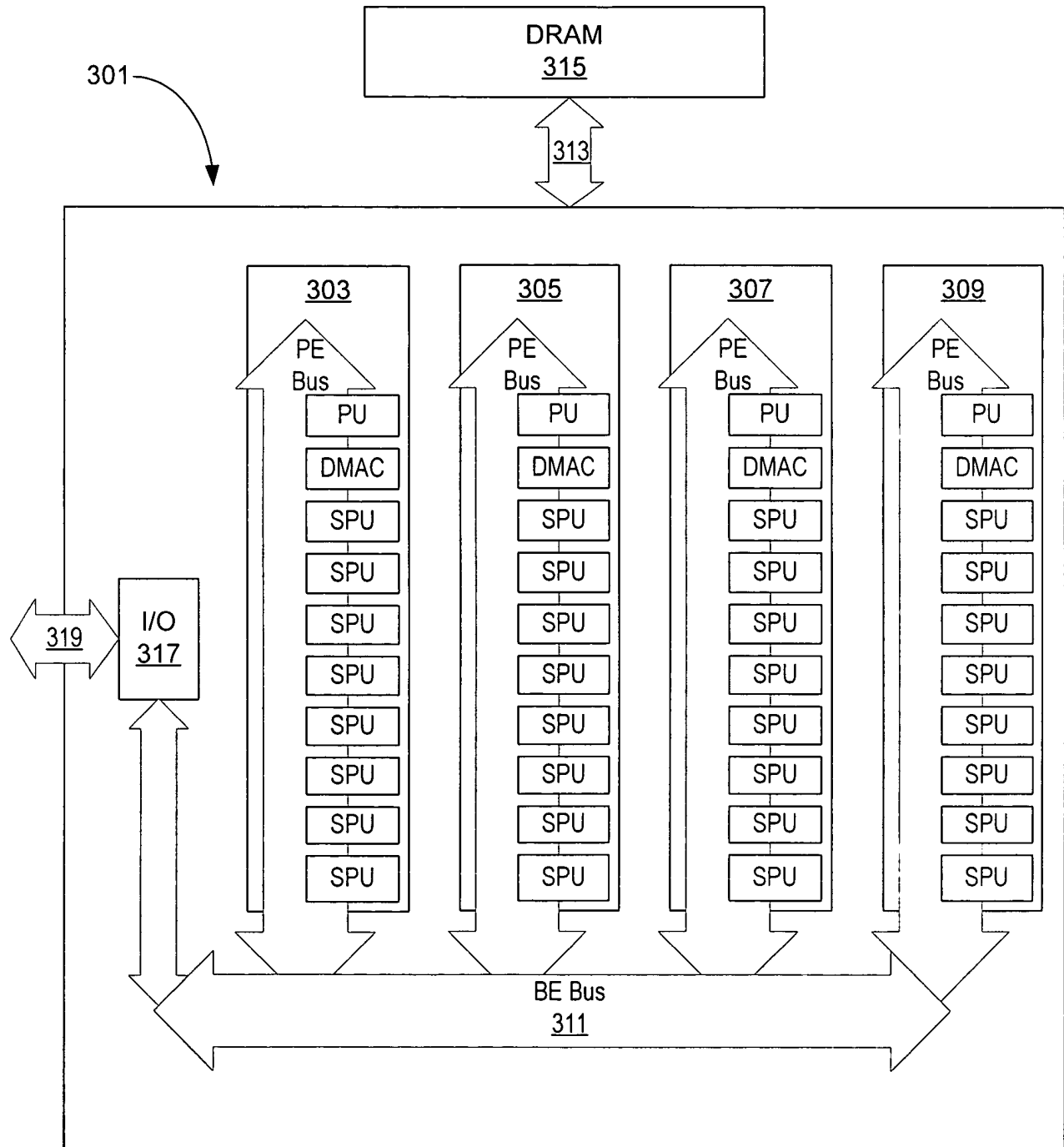
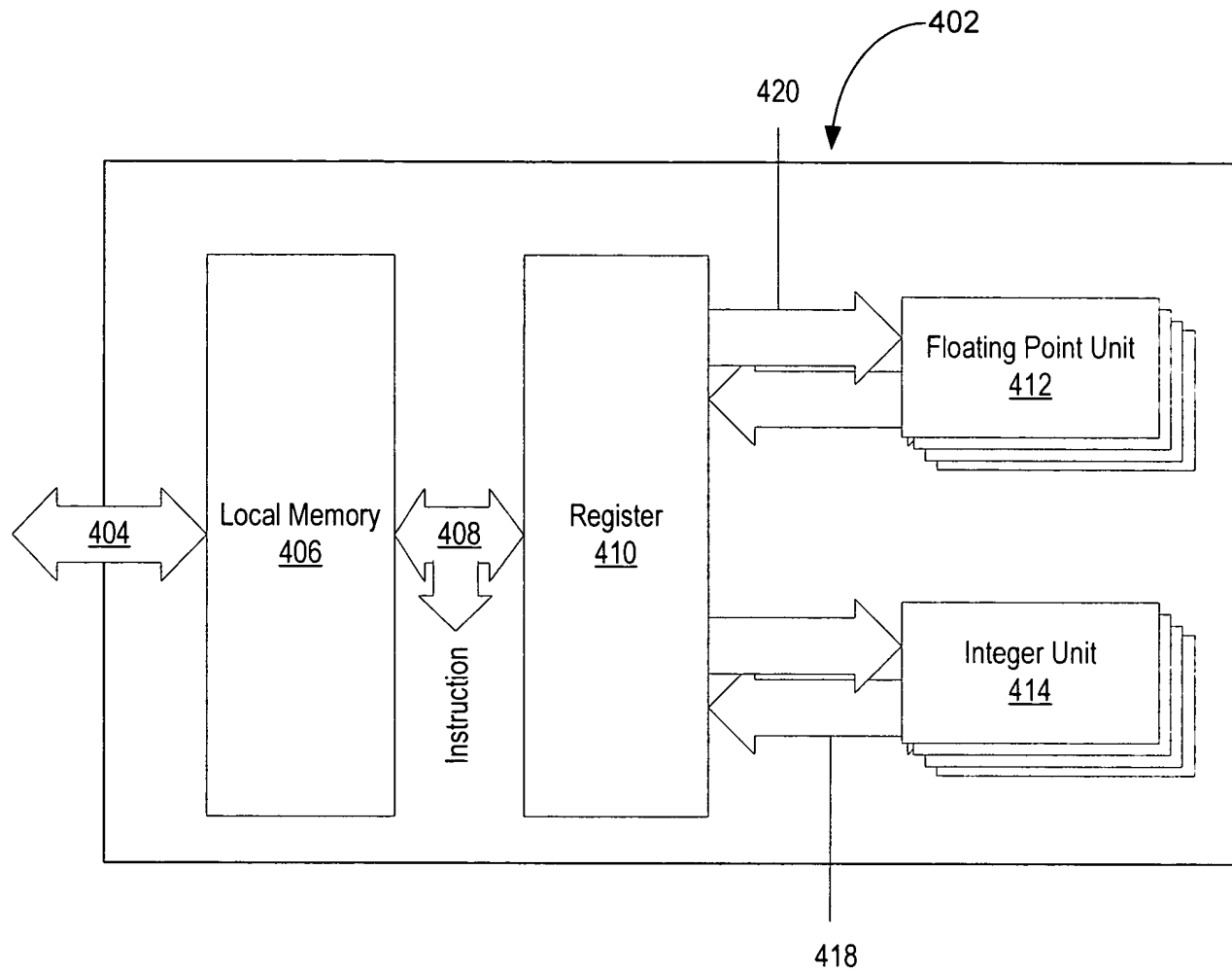
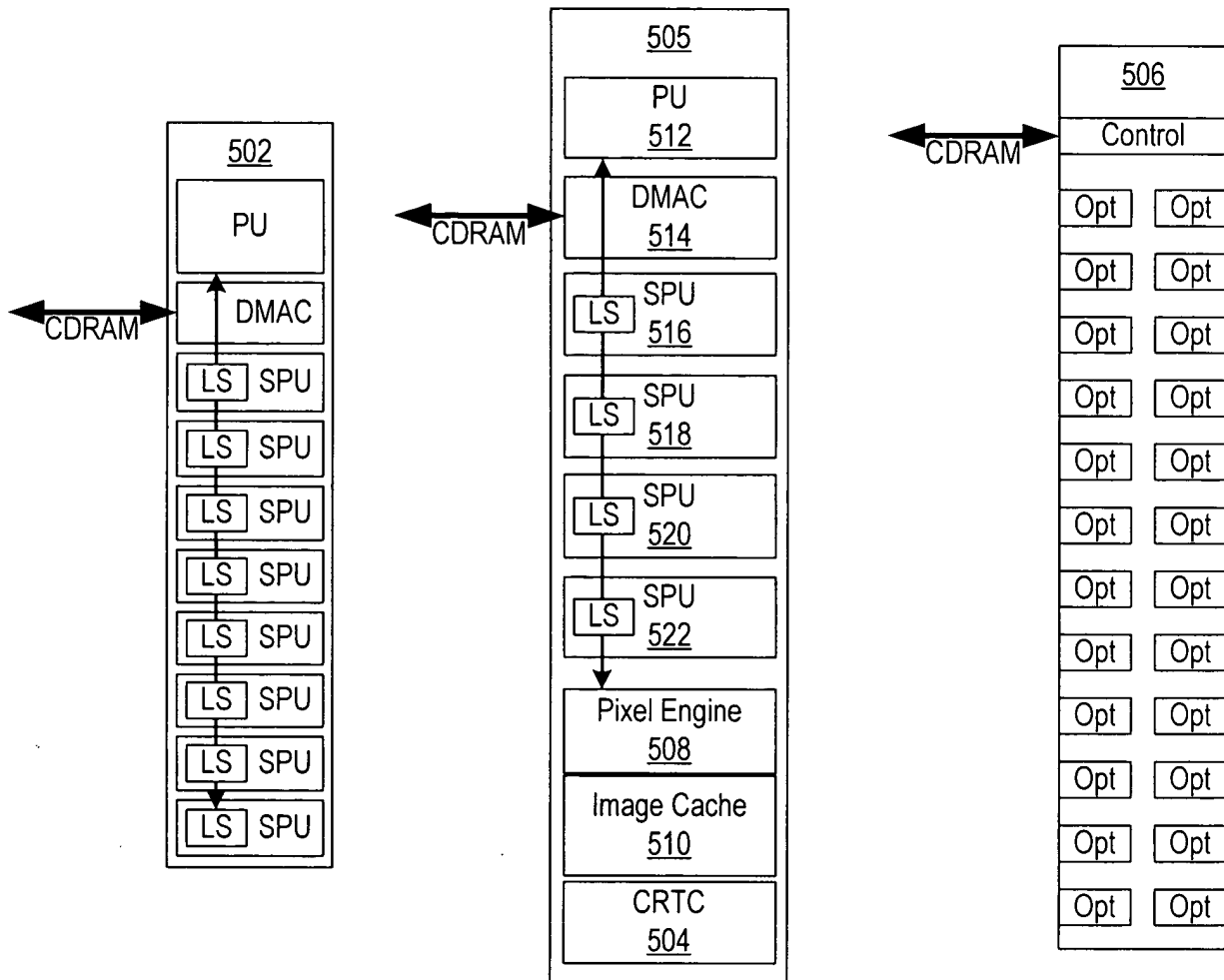


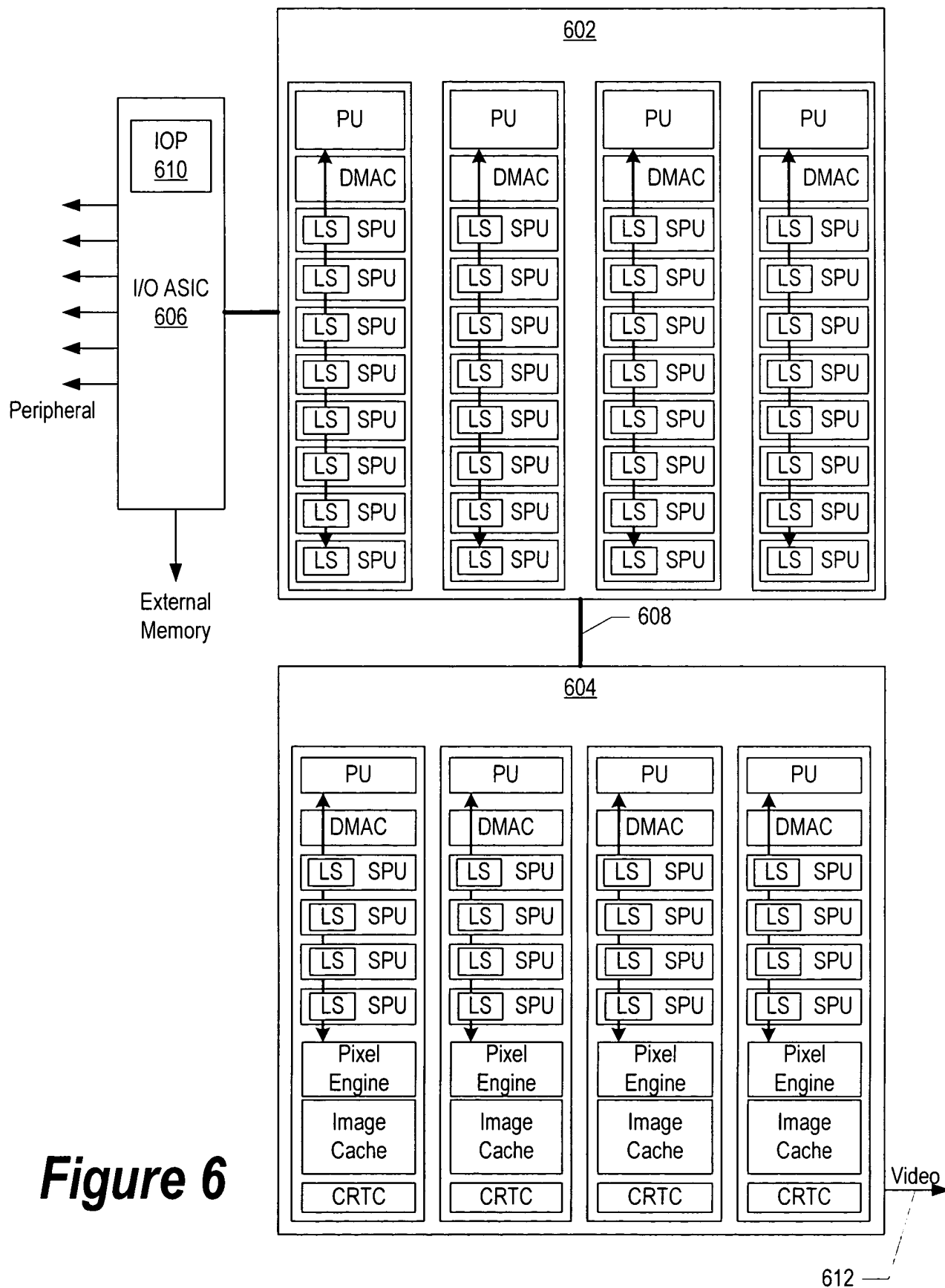
Figure 3

4 / 50

**Figure 4**

**Figure 5**

6 / 50

**Figure 6**

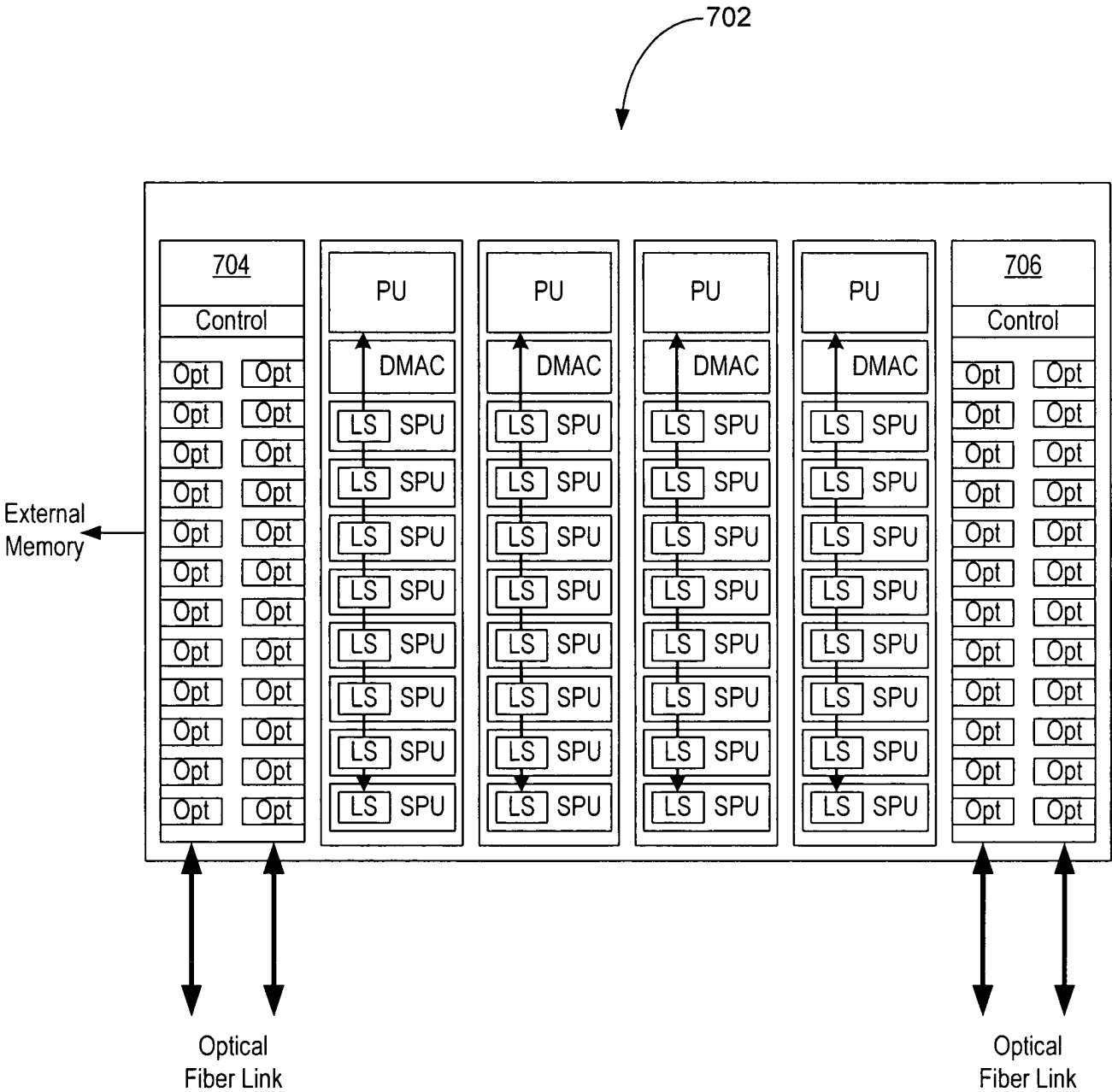


Figure 7

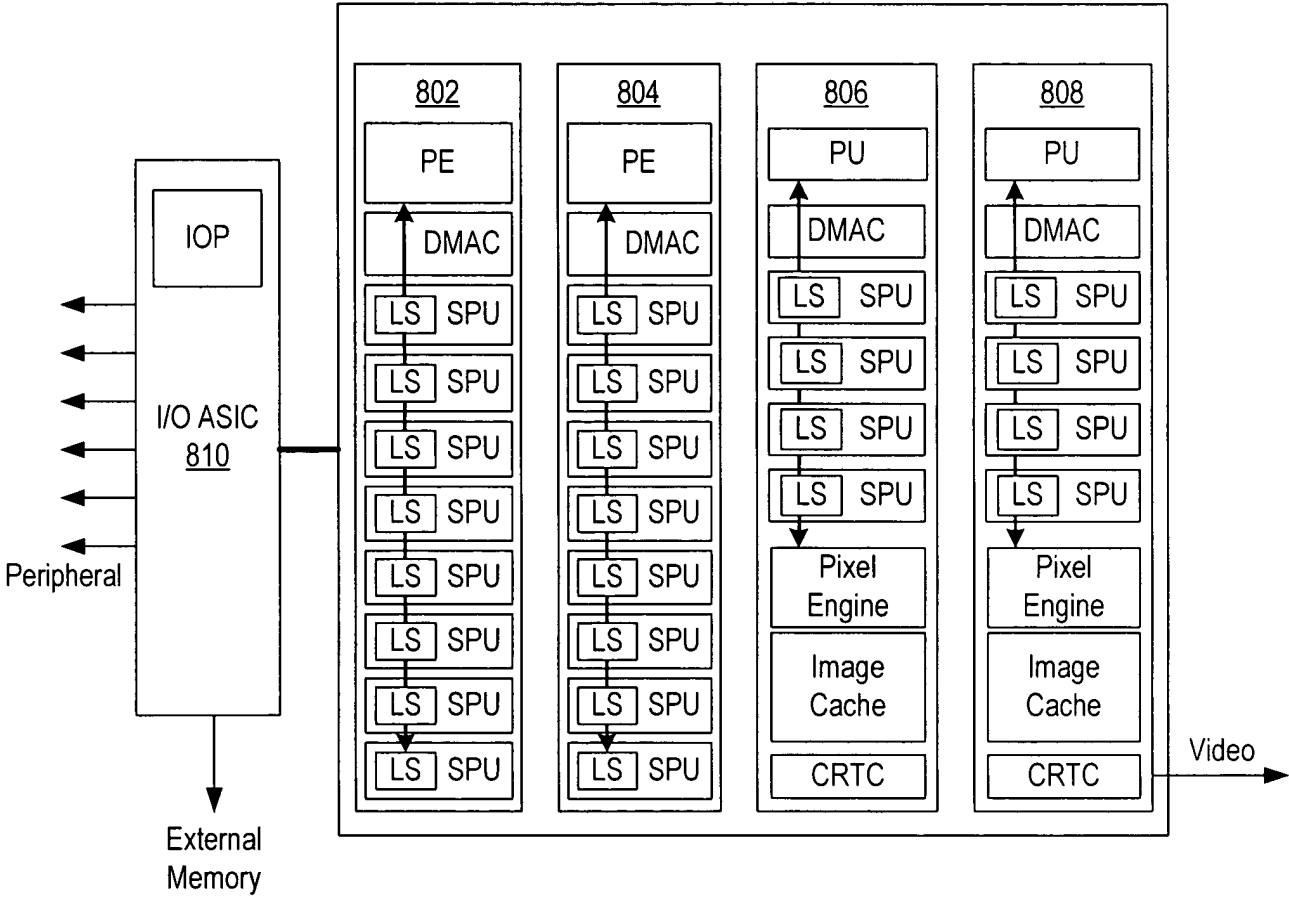
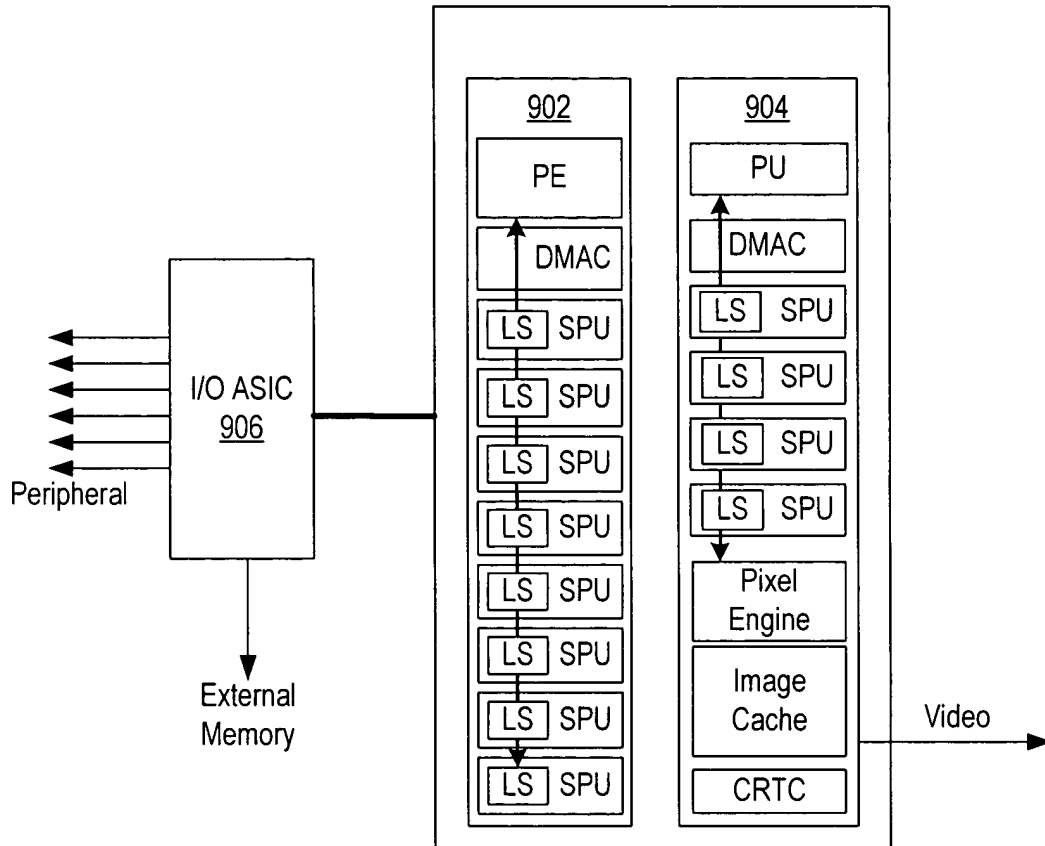
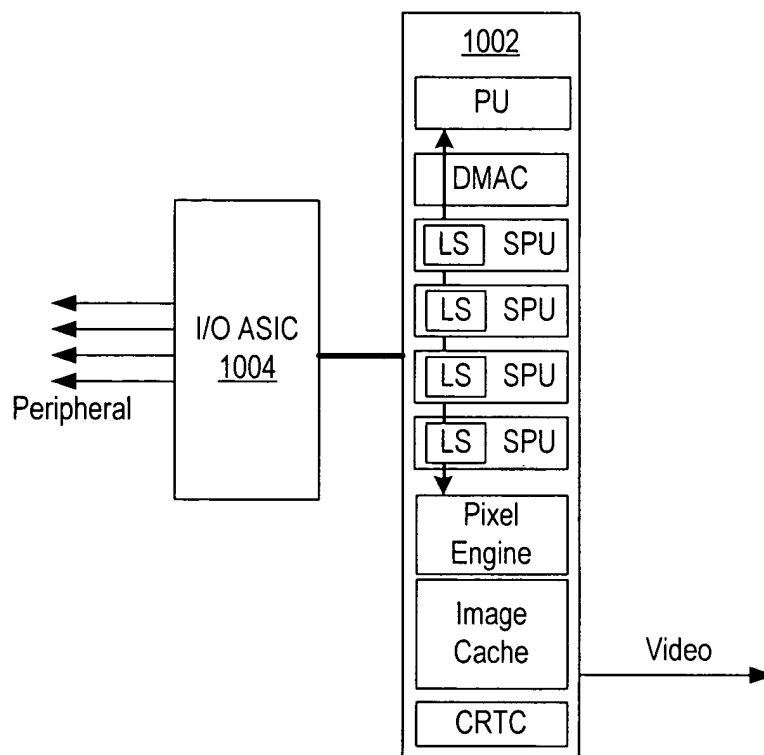
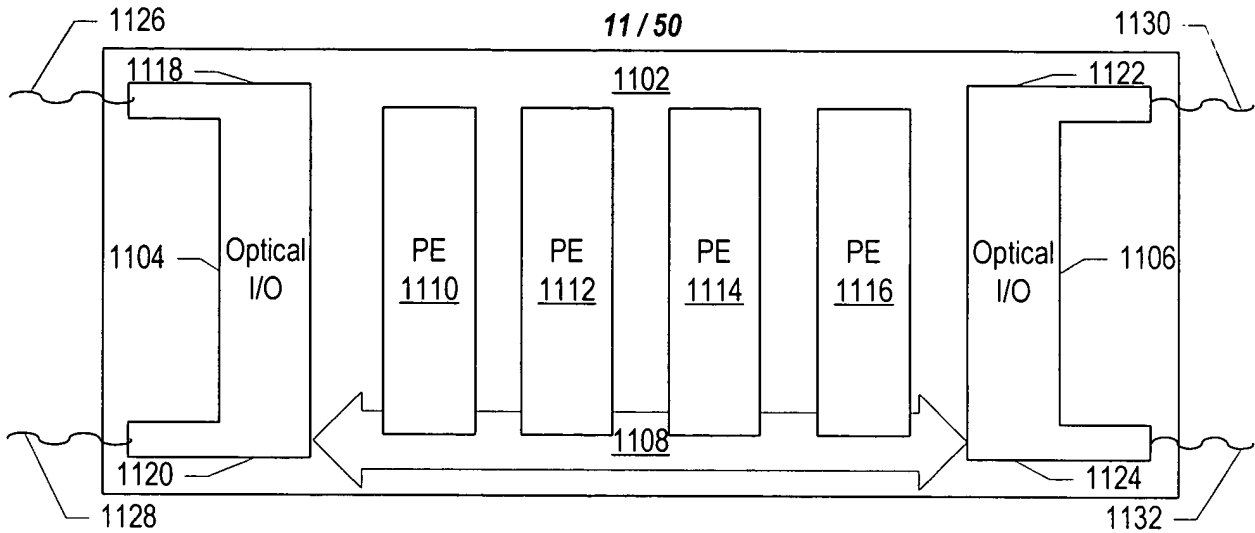
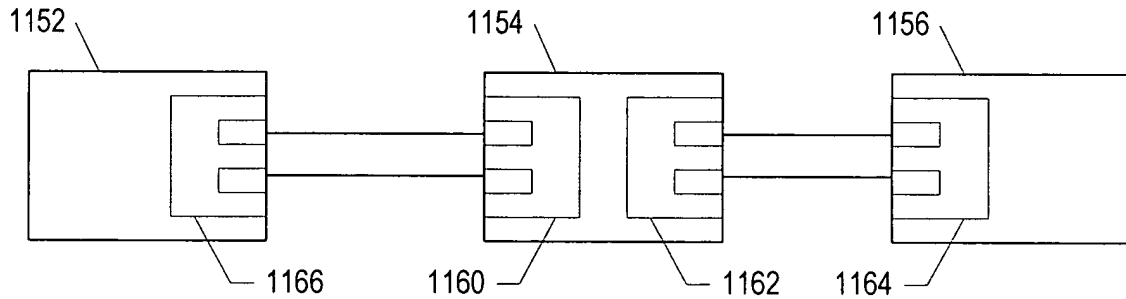
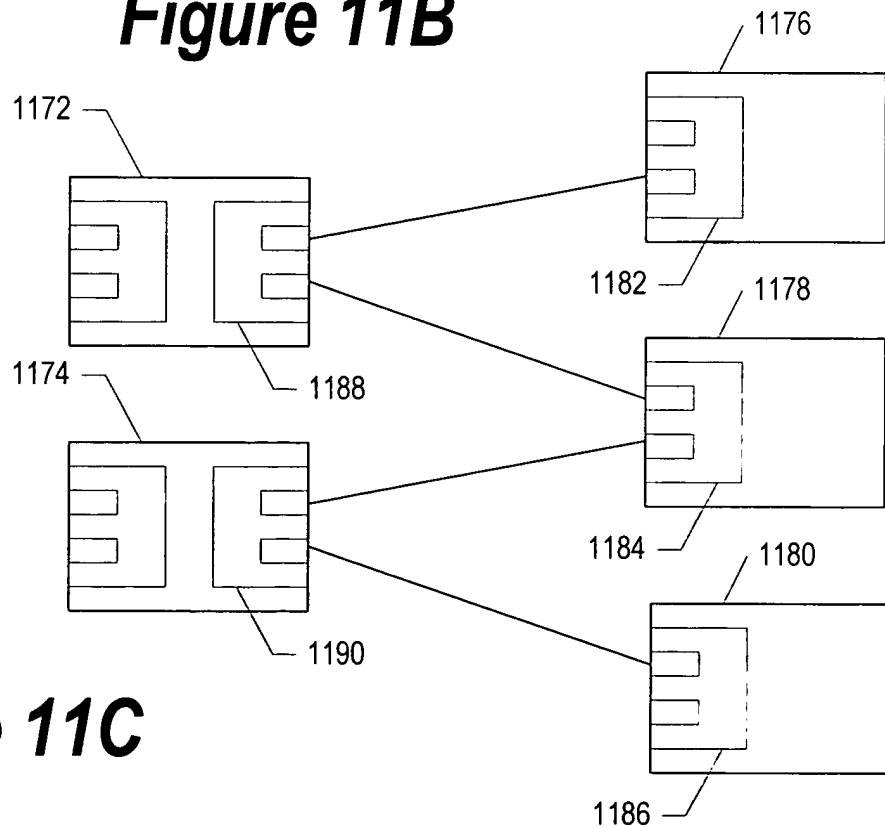


Figure 8

**Figure 9**

**Figure 10**

**Figure 11A****Figure 11B****Figure 11C**

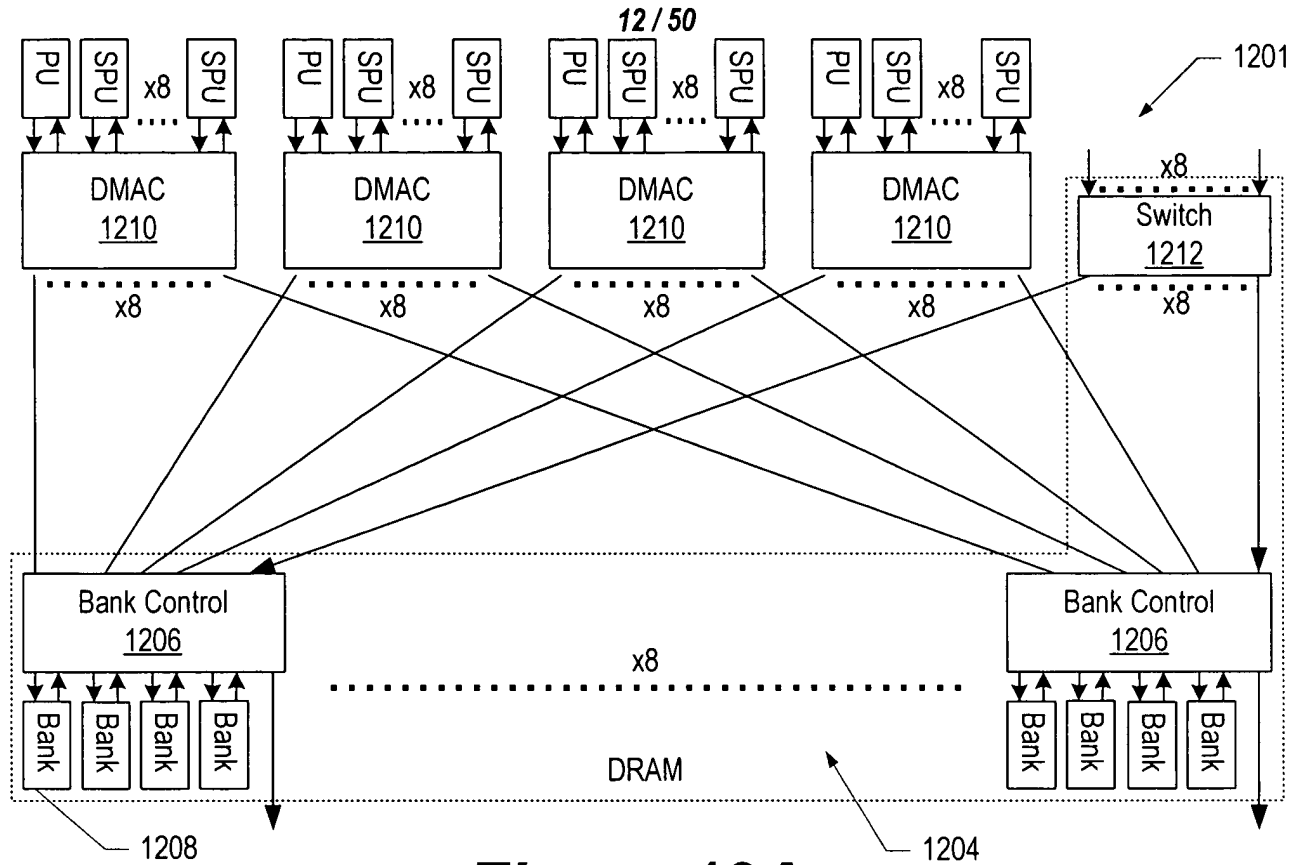


Figure 12A

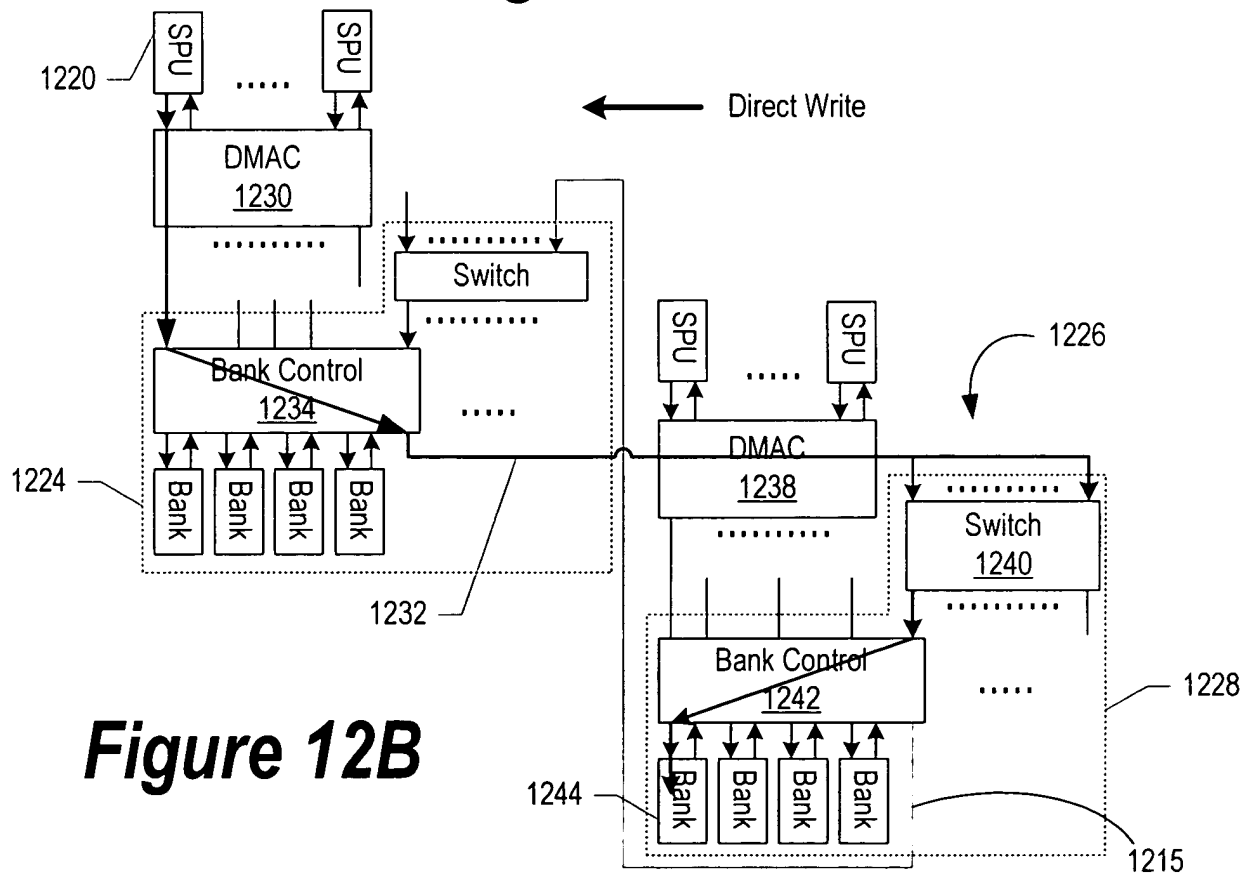


Figure 12B

13 / 50

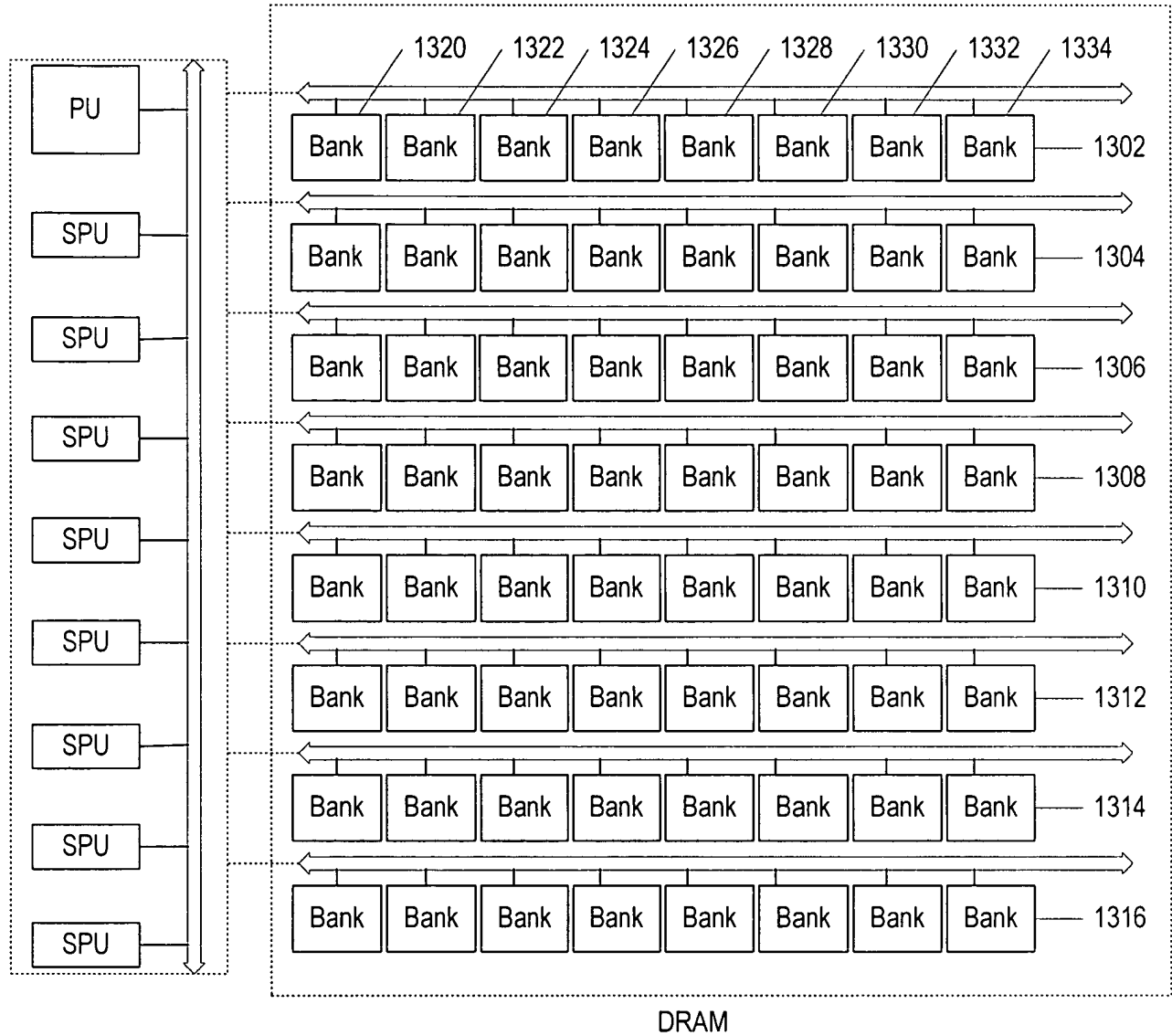


Figure 13

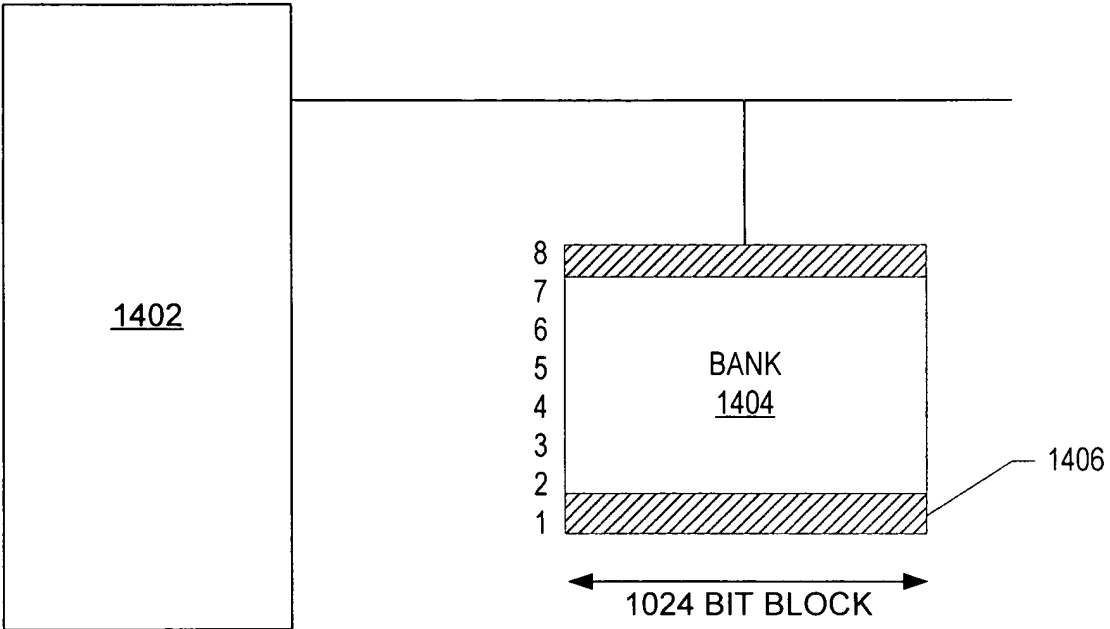


Figure 14A

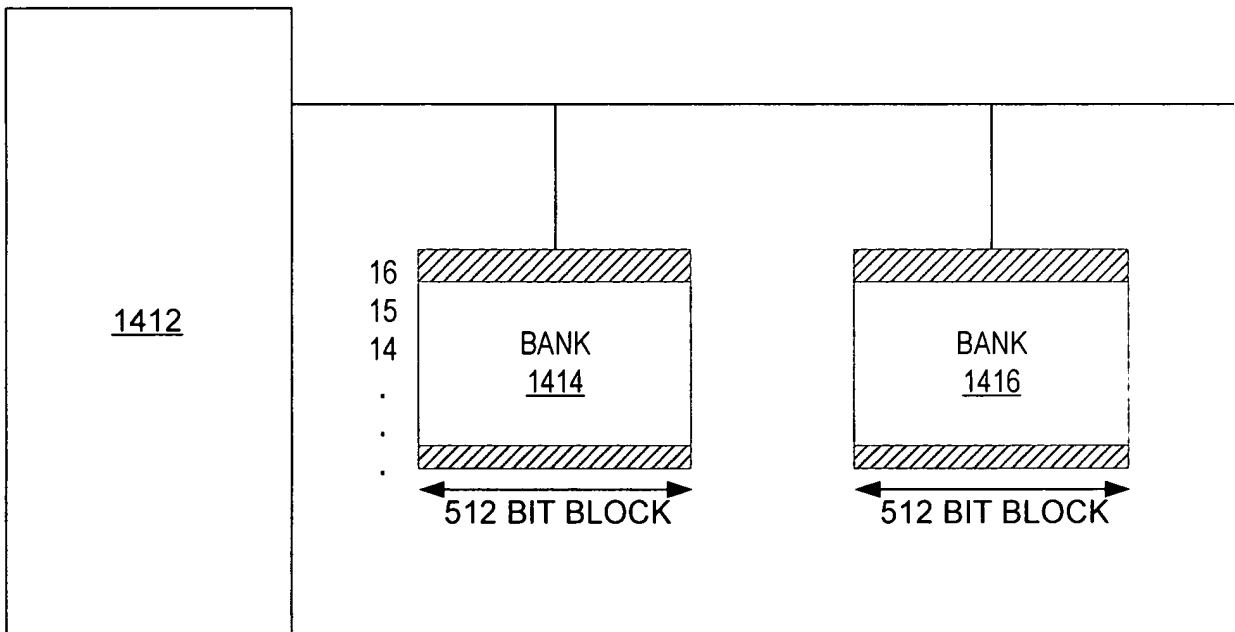


Figure 14B

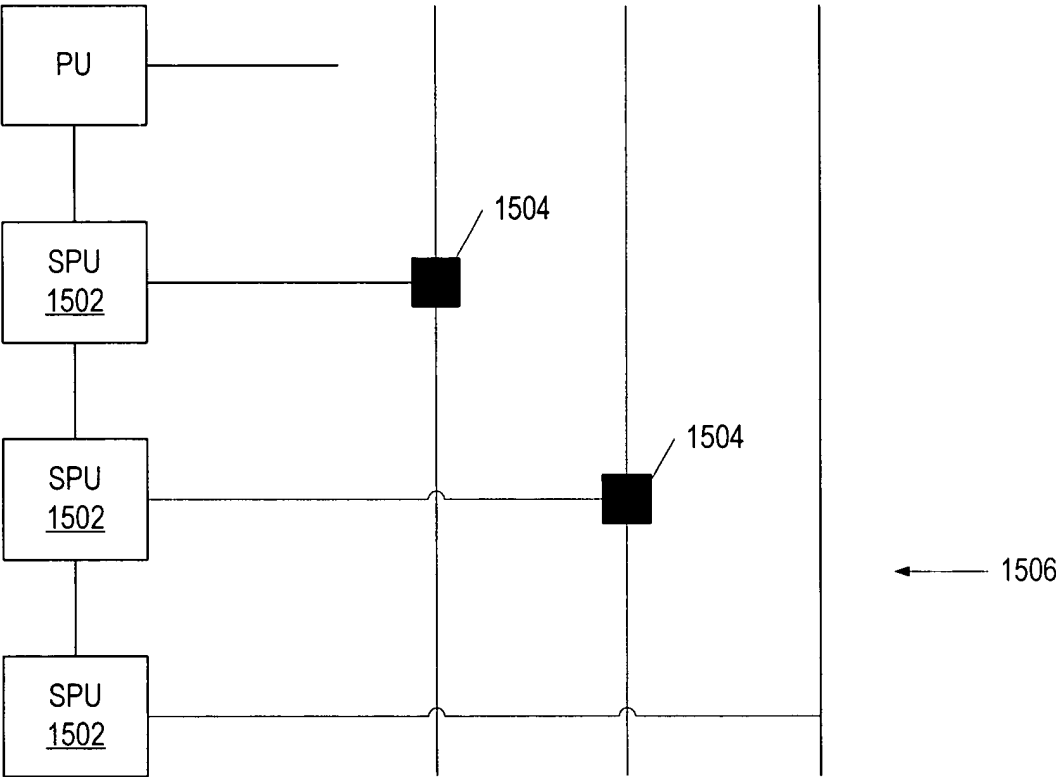
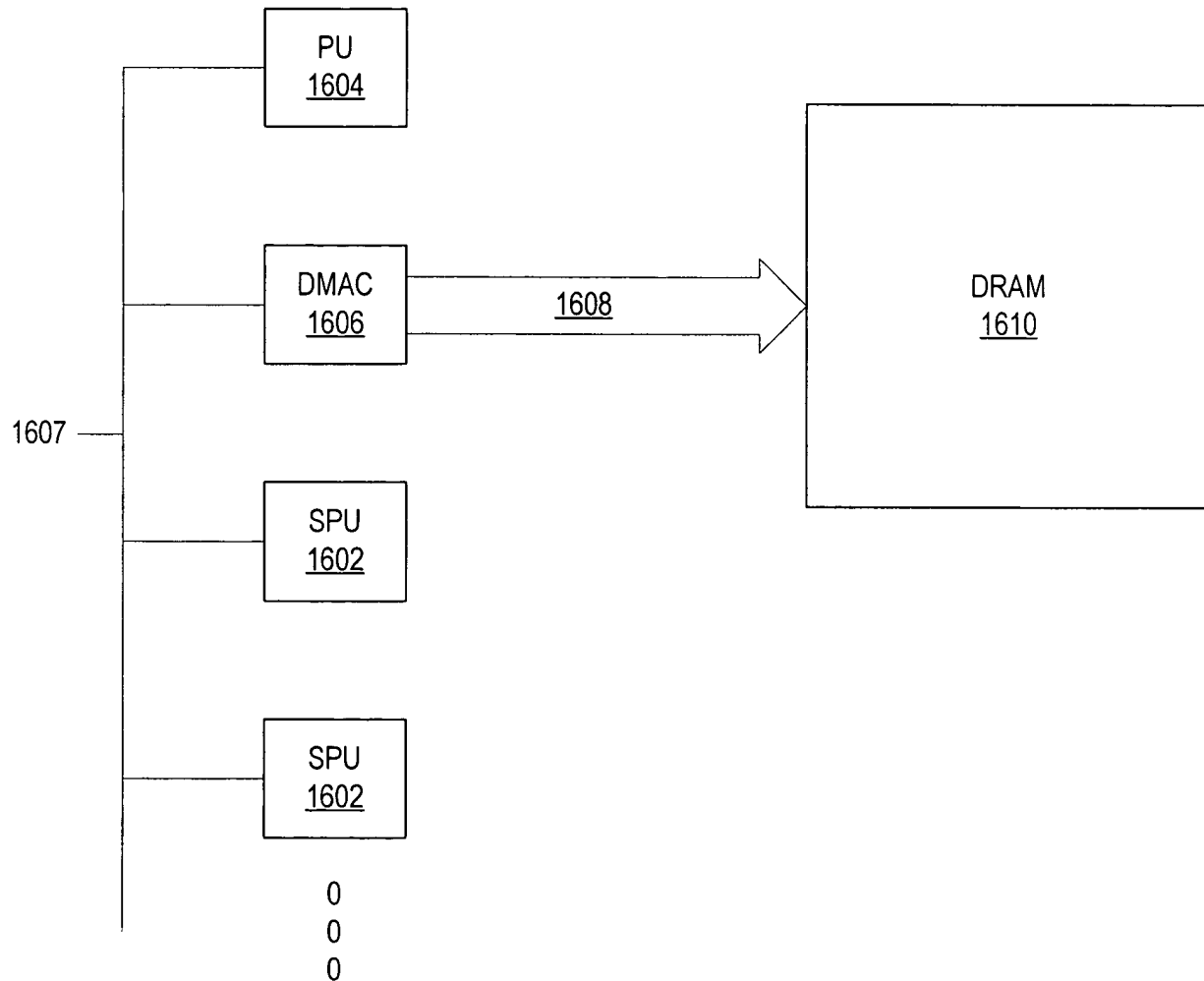
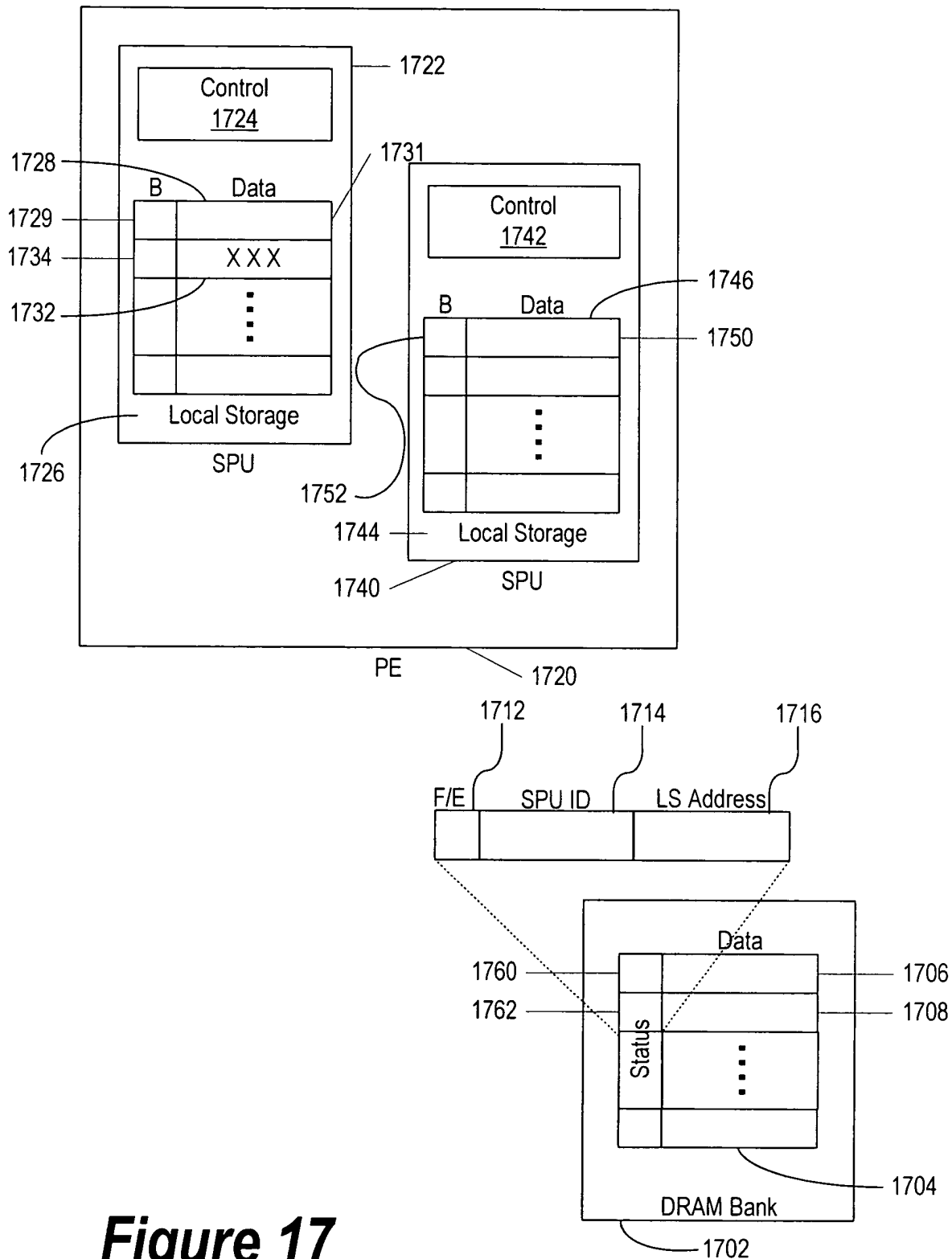


Figure 15

**Figure 16**



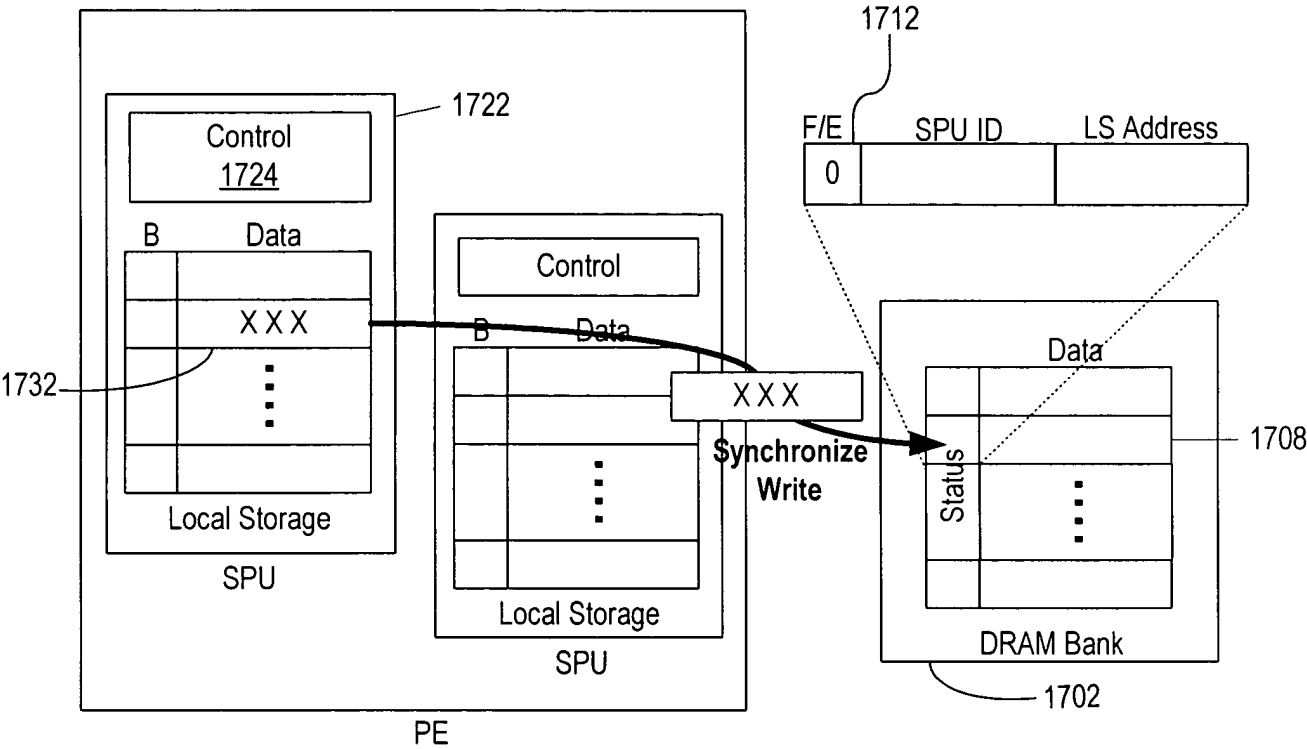


Figure 18

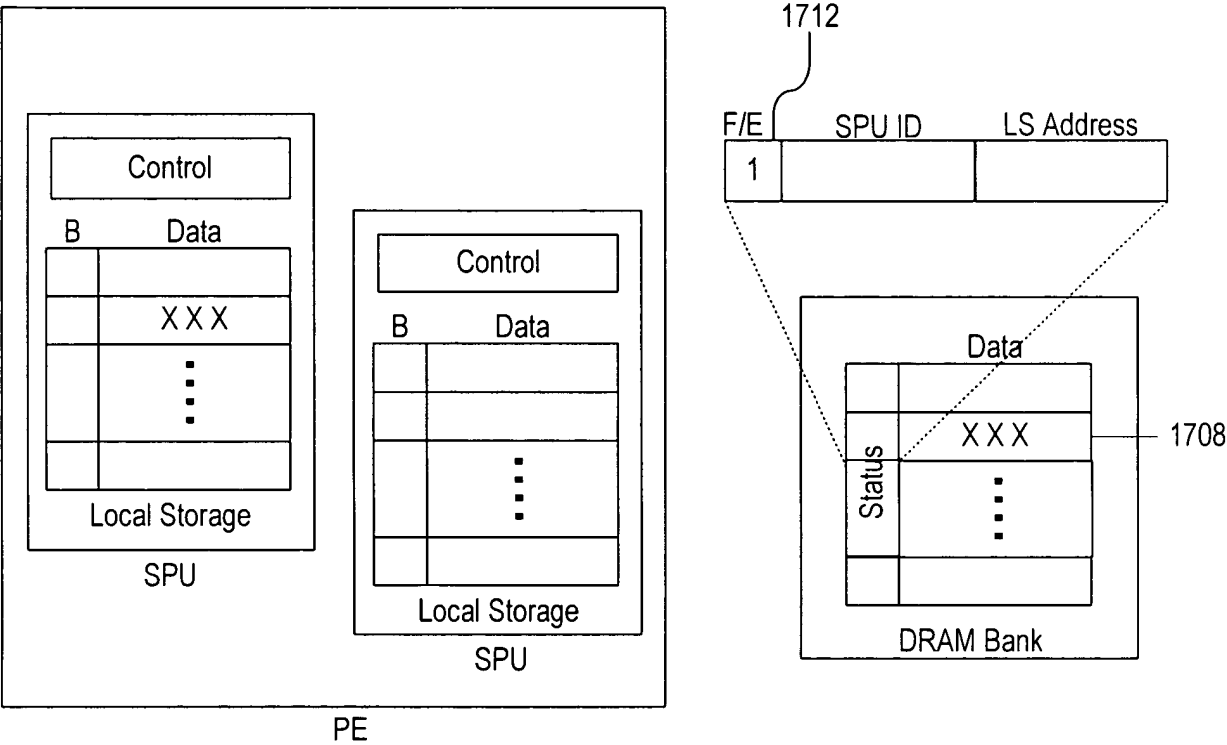
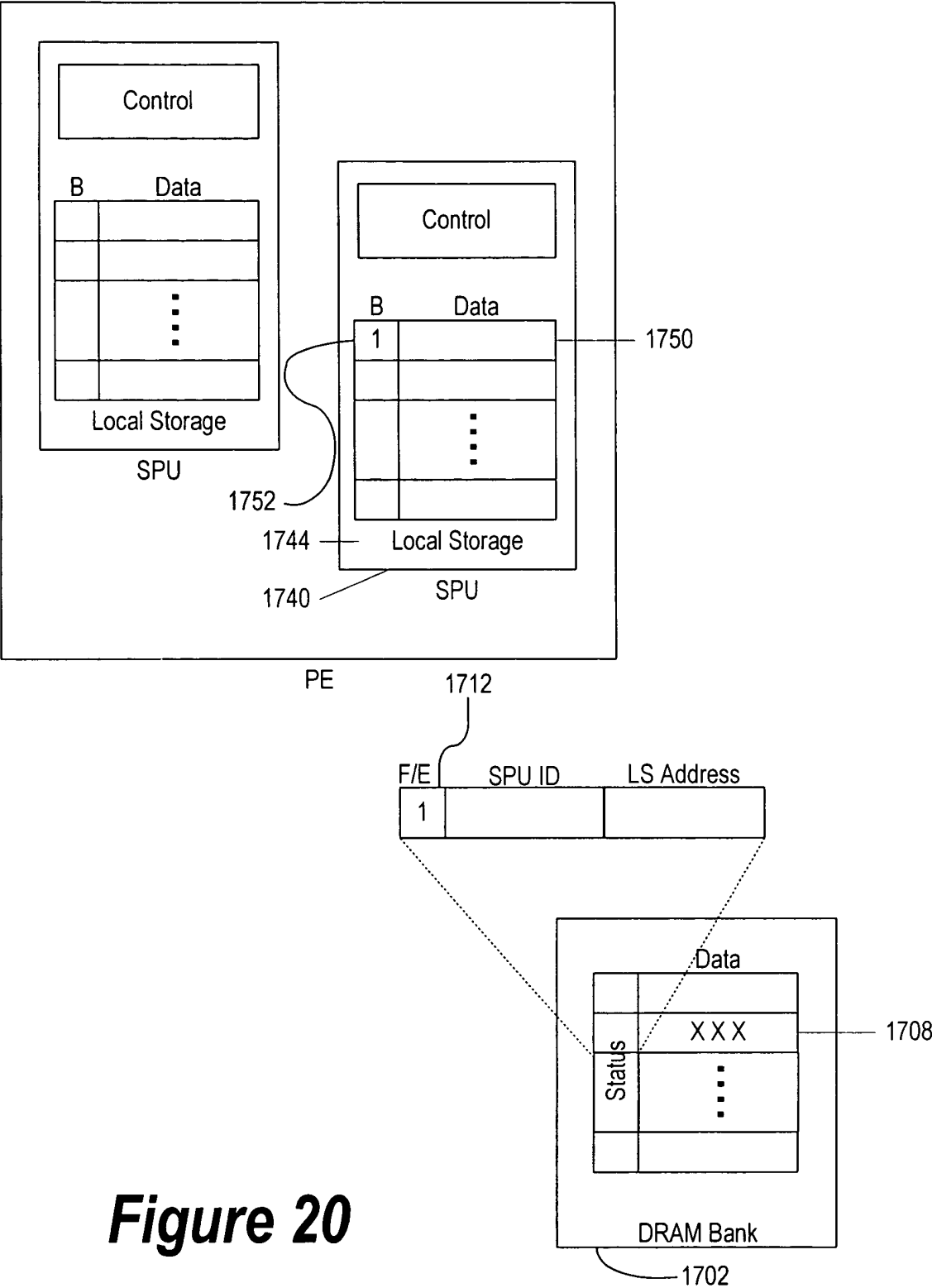
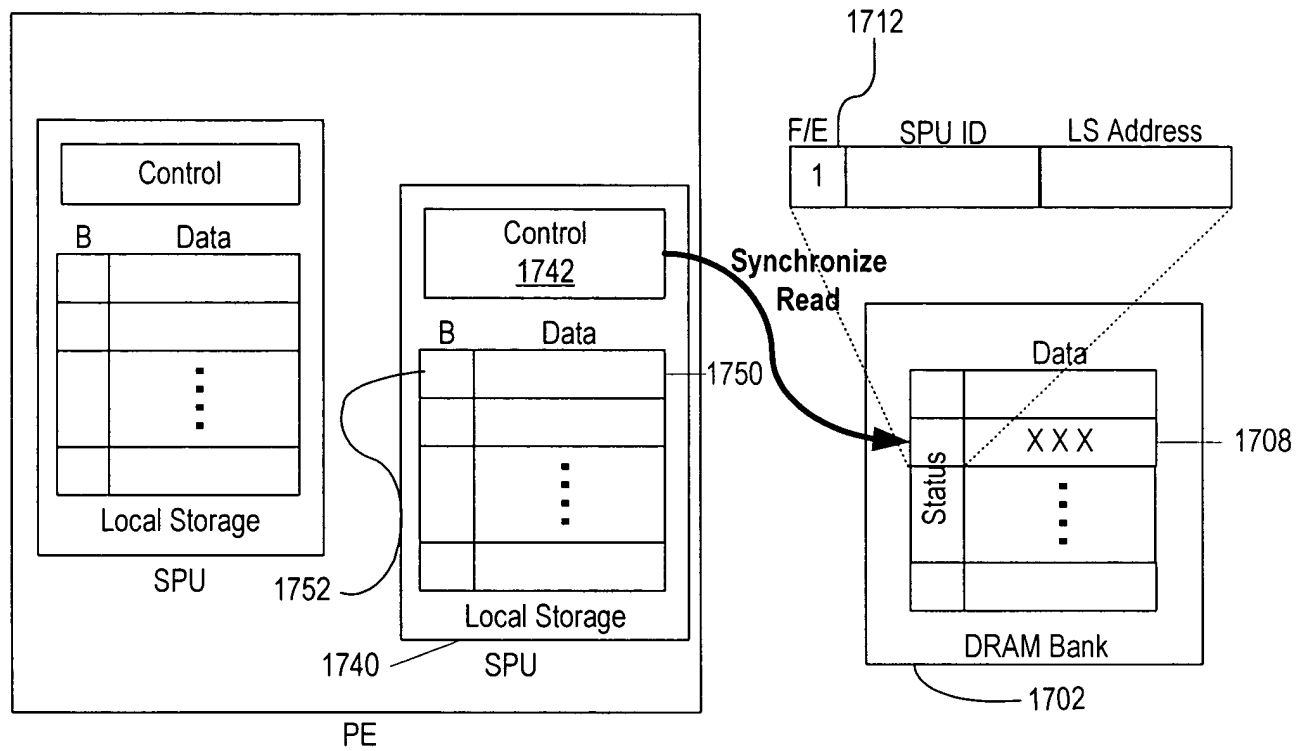
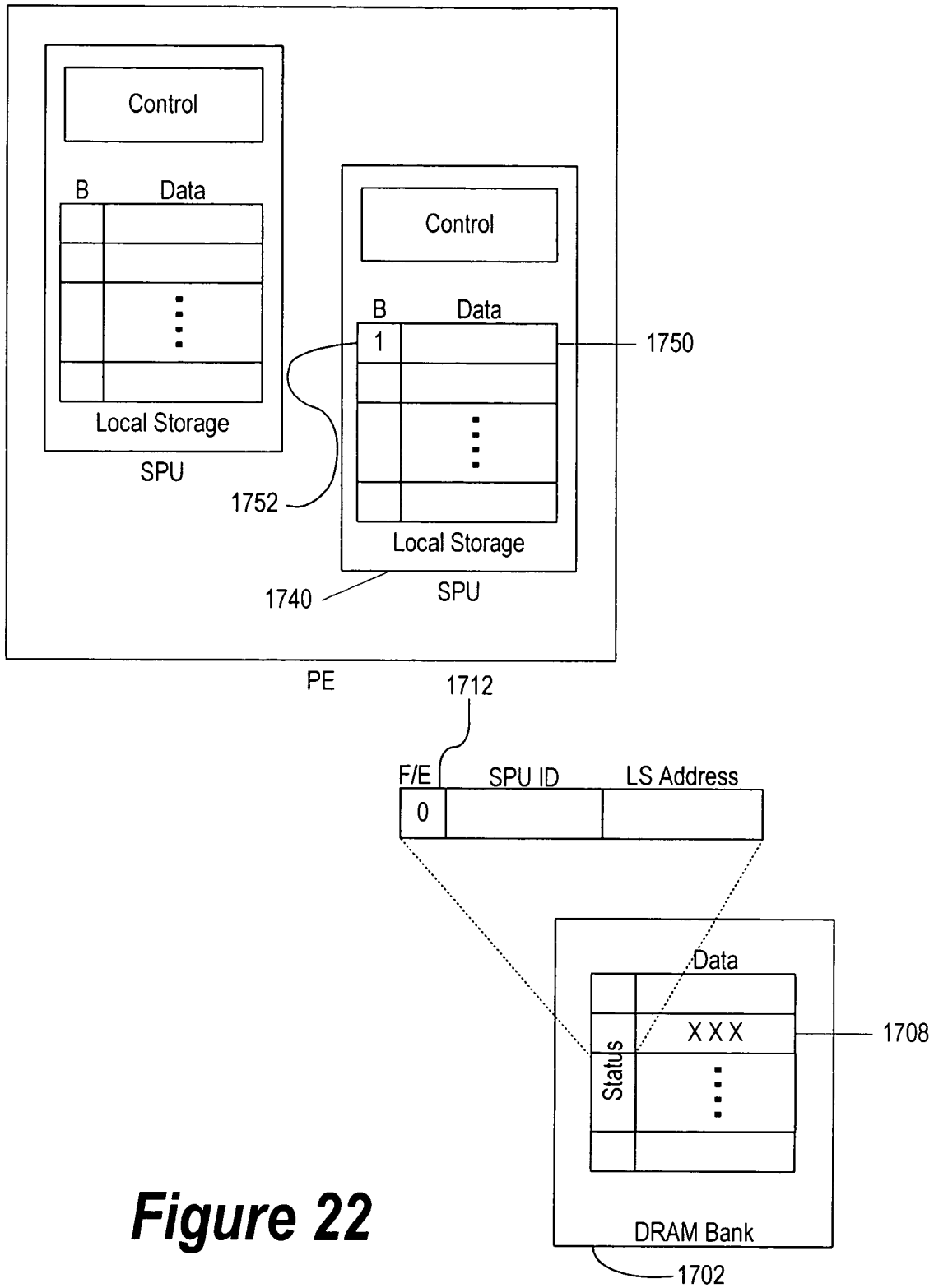
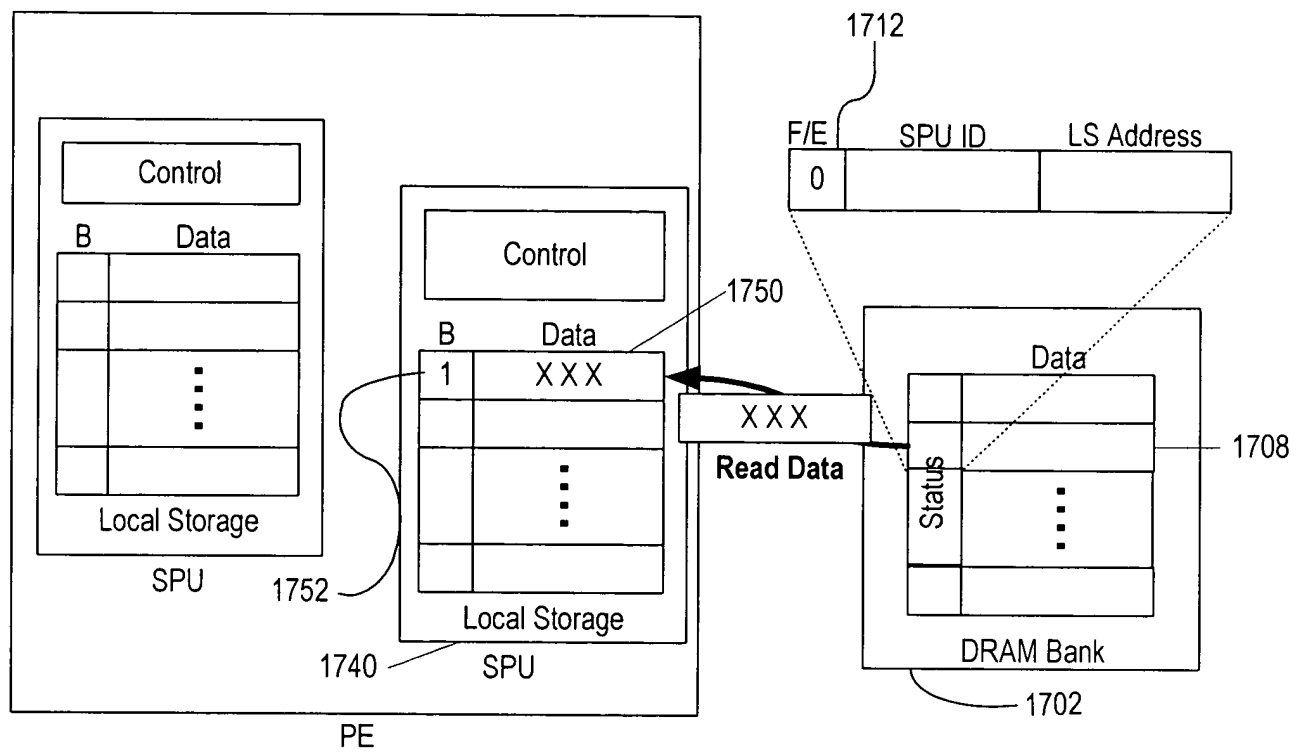


Figure 19

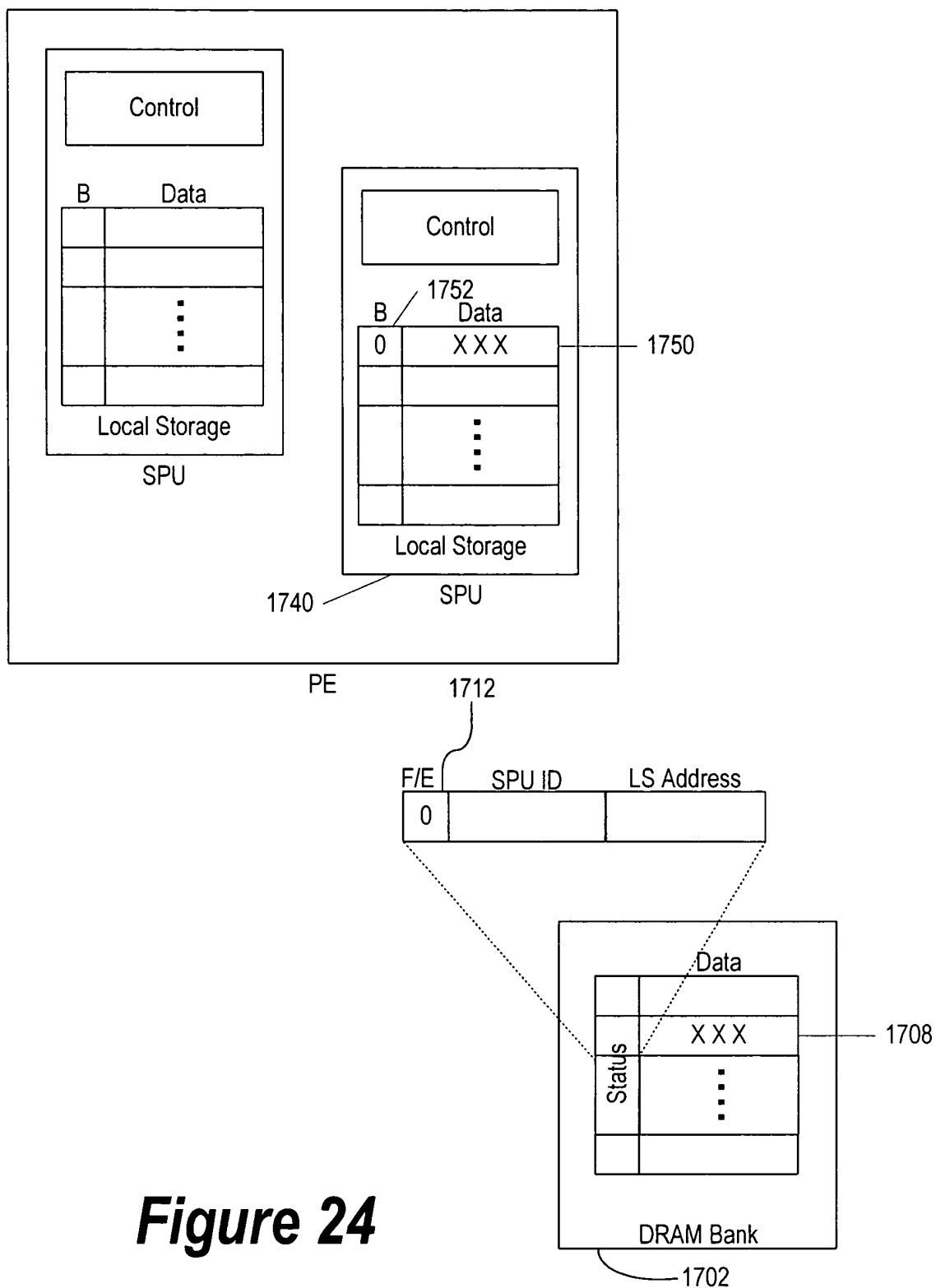


**Figure 21**

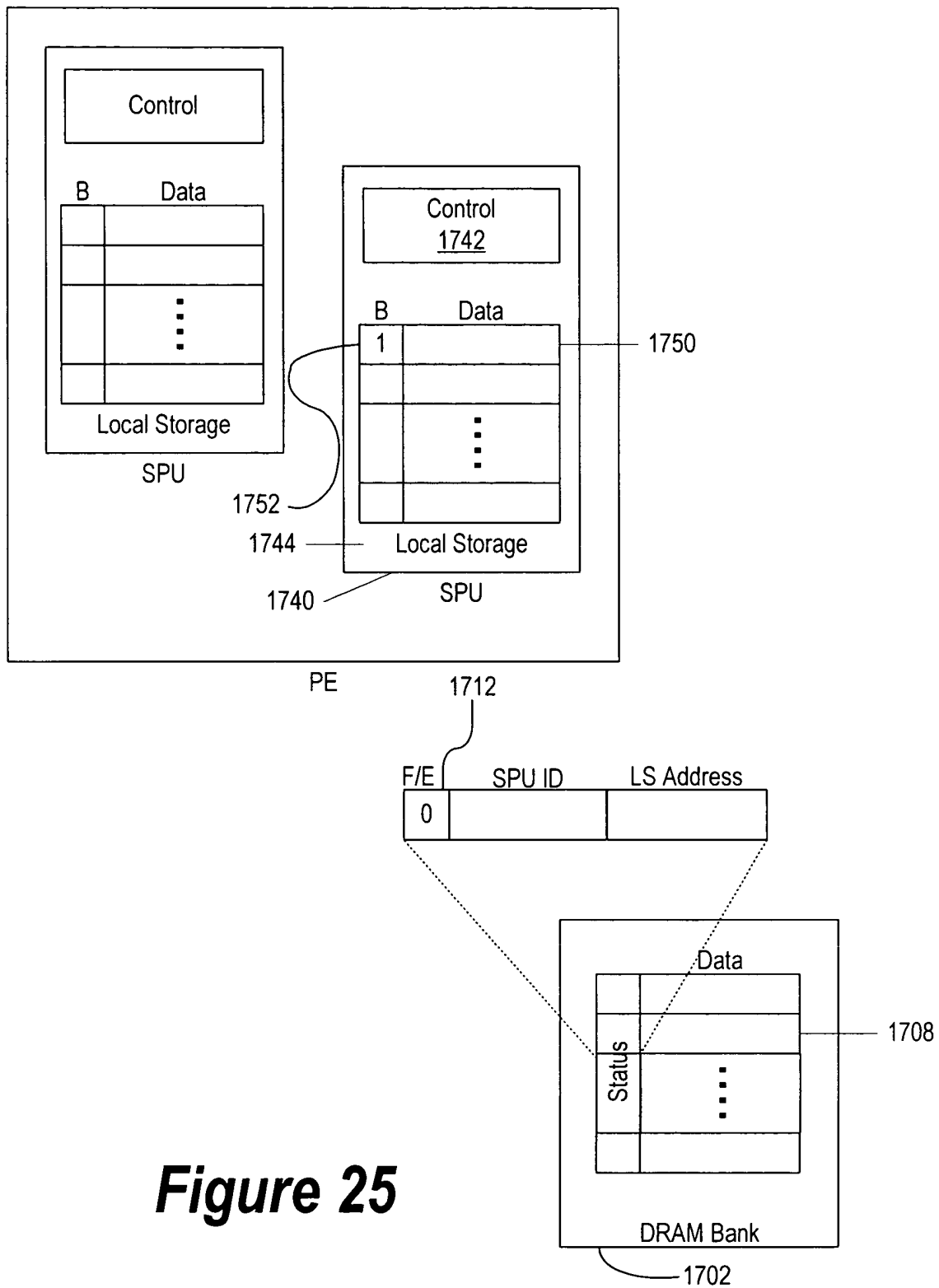


**Figure 23**

24 / 50

**Figure 24**

25 / 50

**Figure 25**

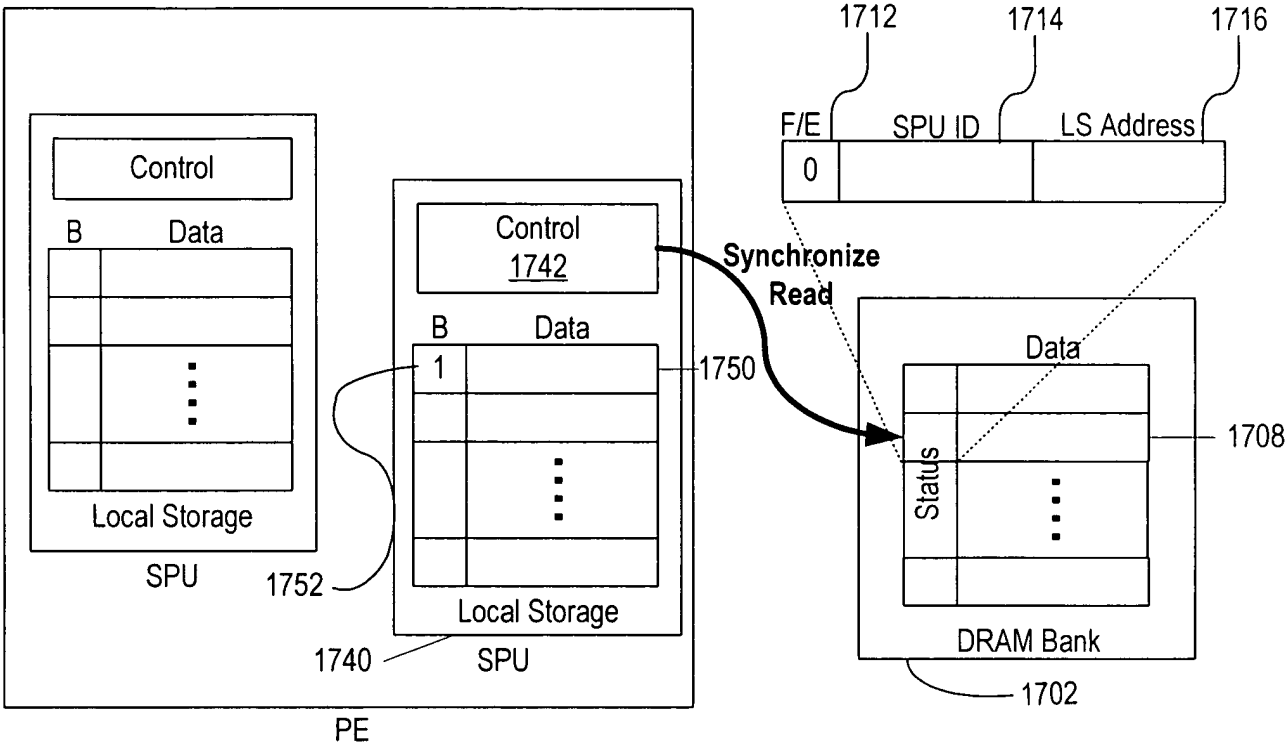


Figure 26

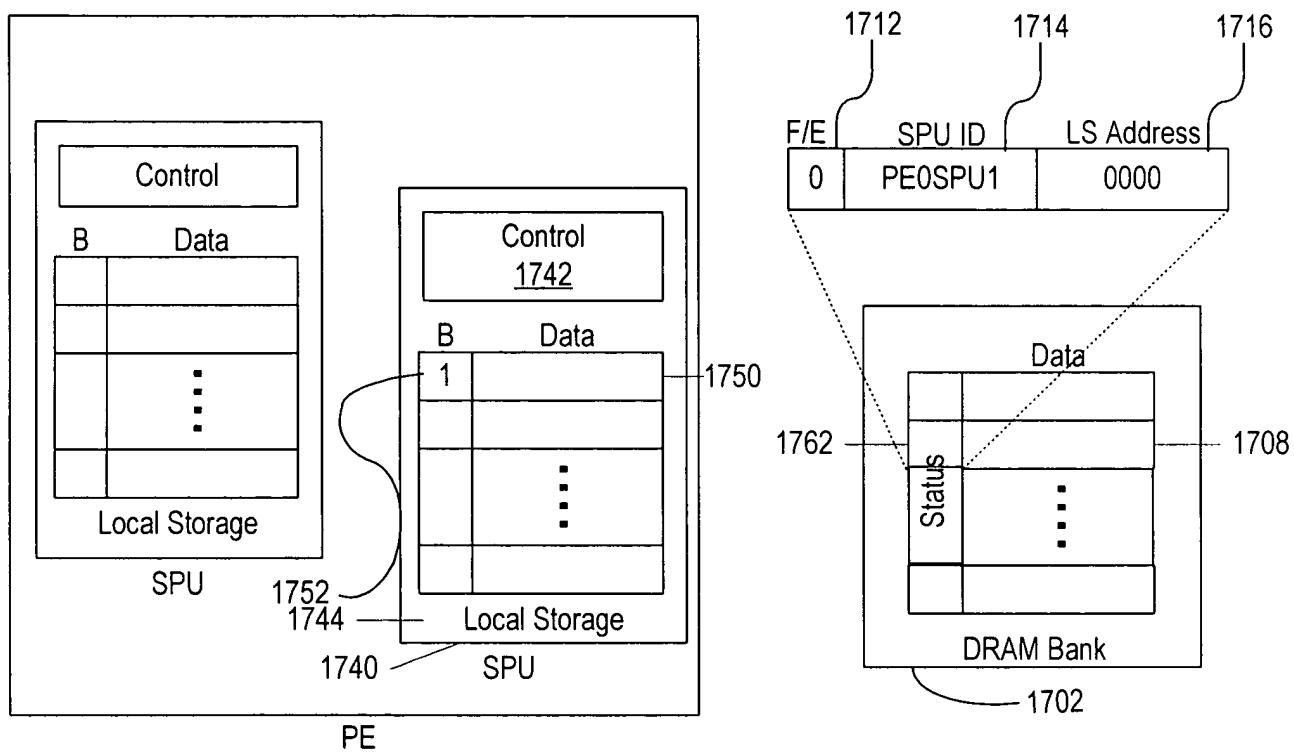
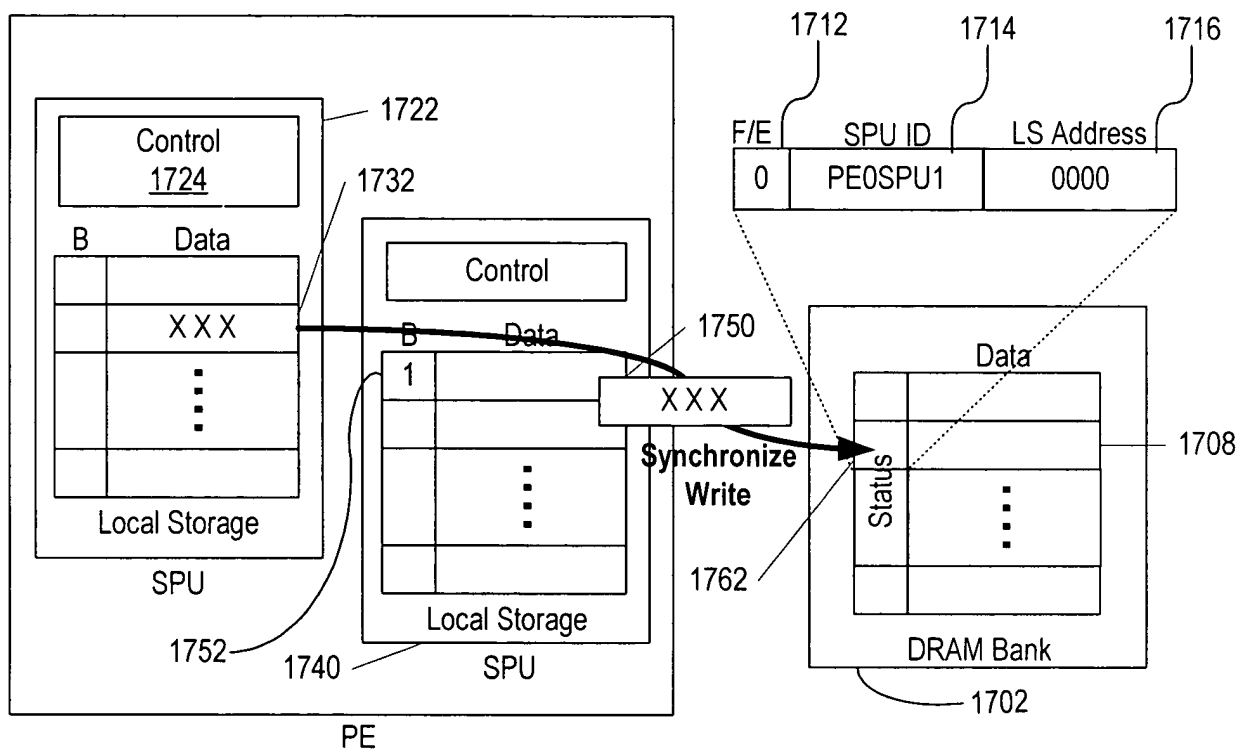


Figure 27

**Figure 28**

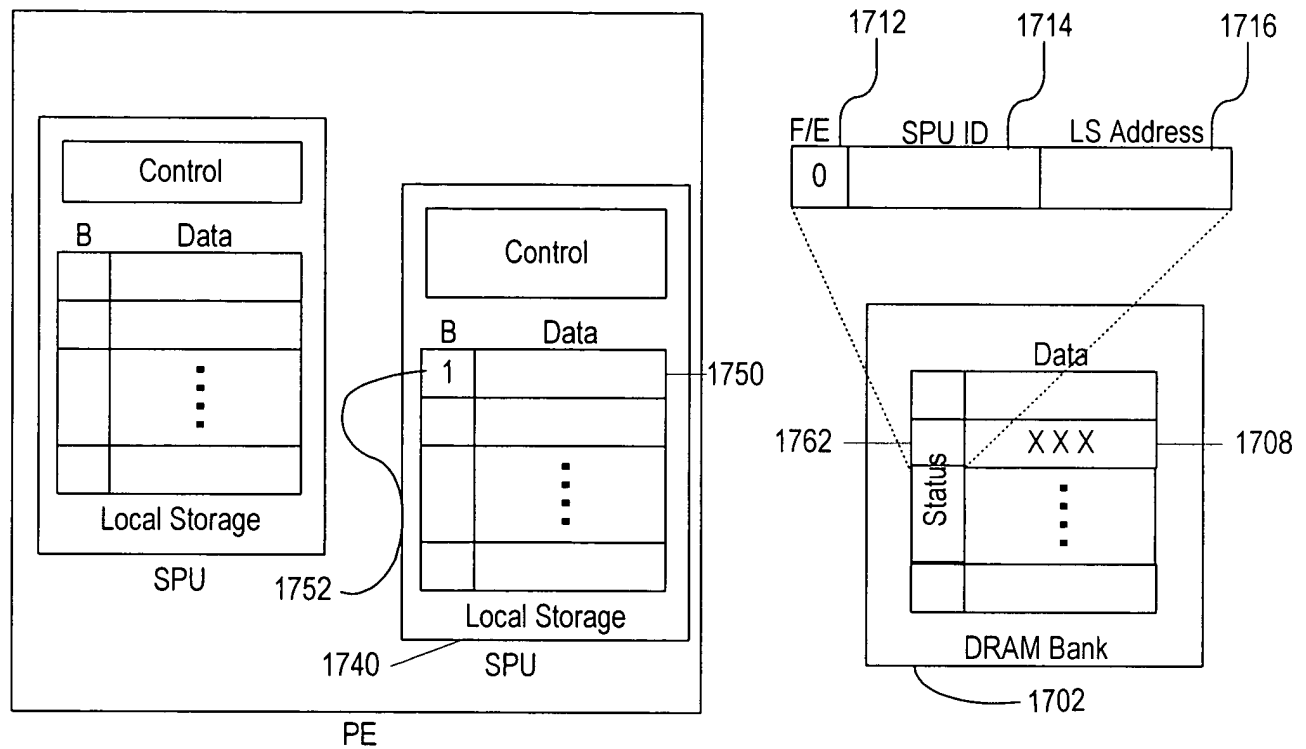


Figure 29

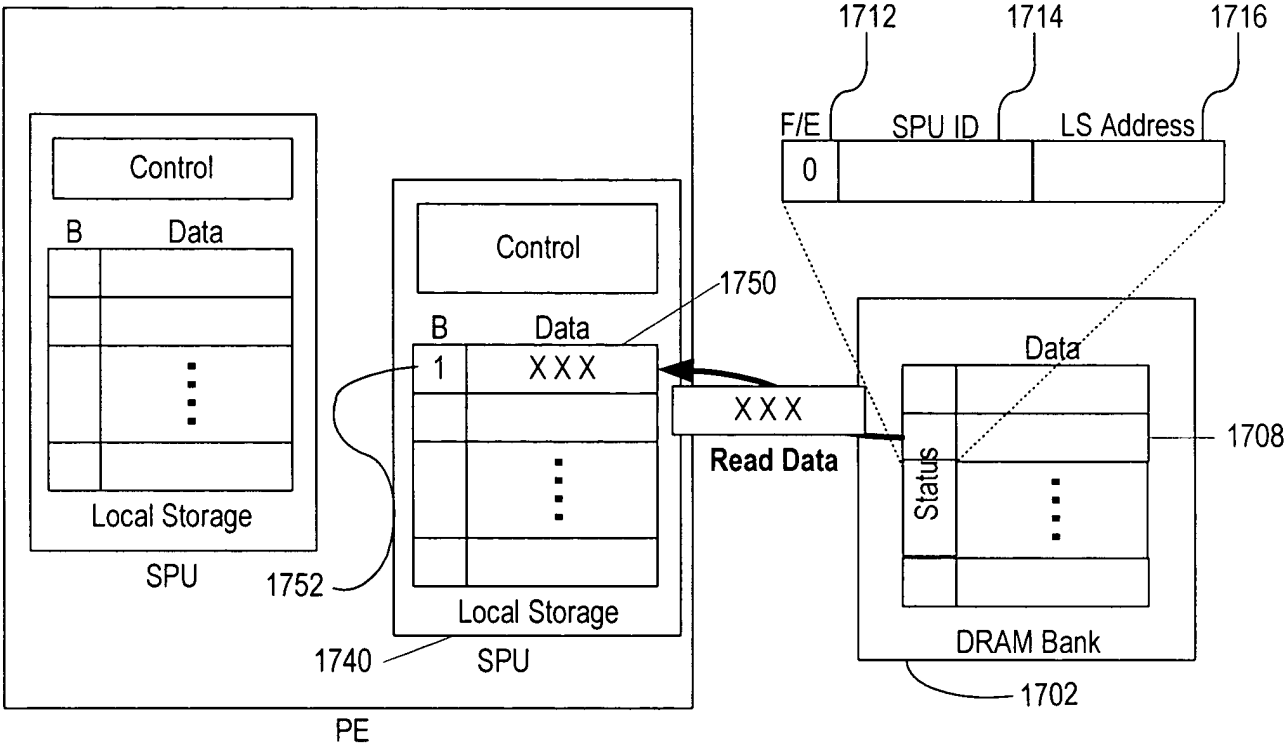


Figure 30

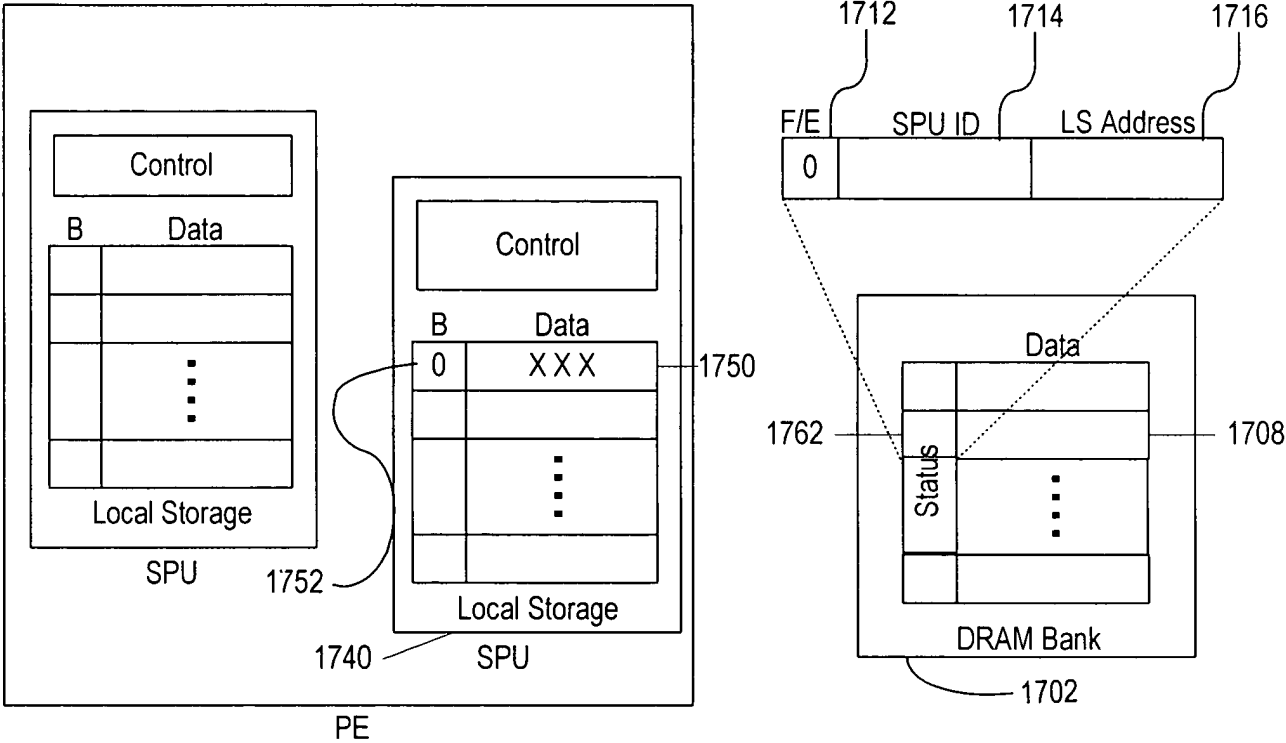
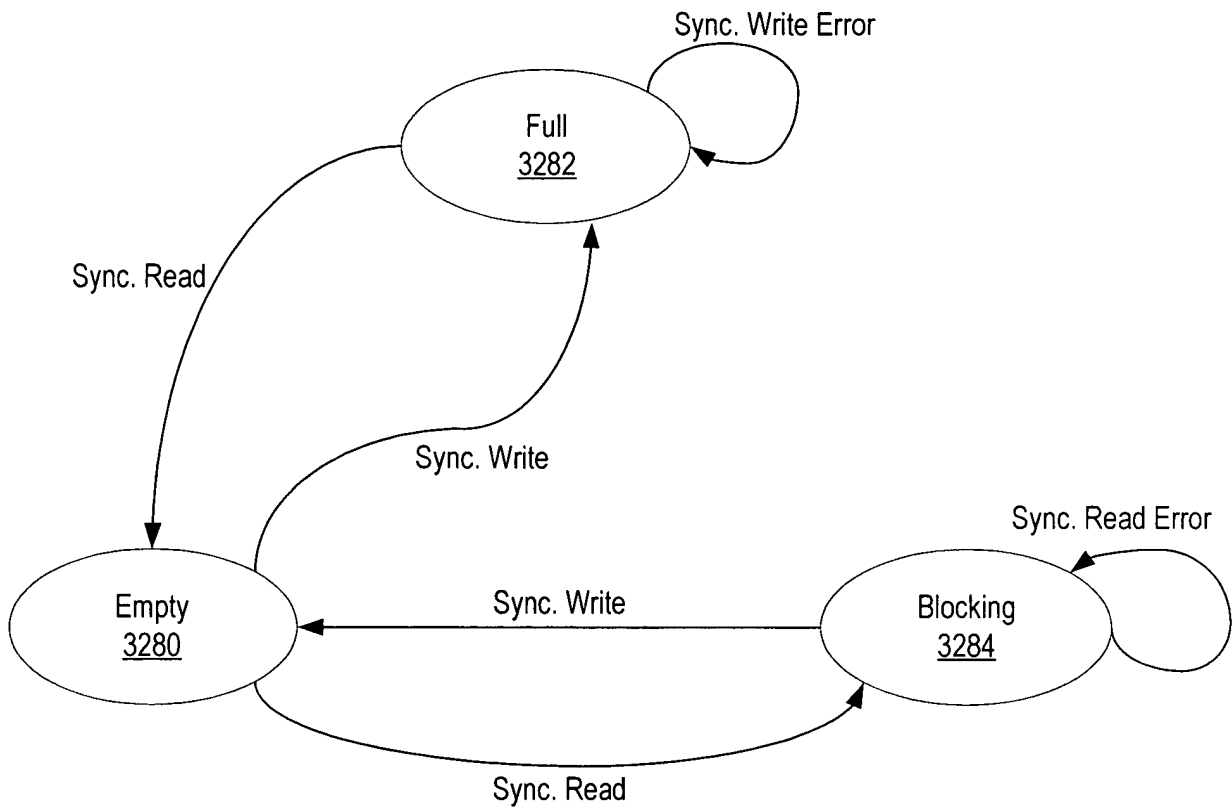


Figure 31

**Figure 32**

Key Control Table

3302

3304 ID 3306 3308

0	SPU Key	Key Mask
1	SPU Key	Key Mask
2	SPU Key	Key Mask
7	SPU Key	Key Mask

Figure 33

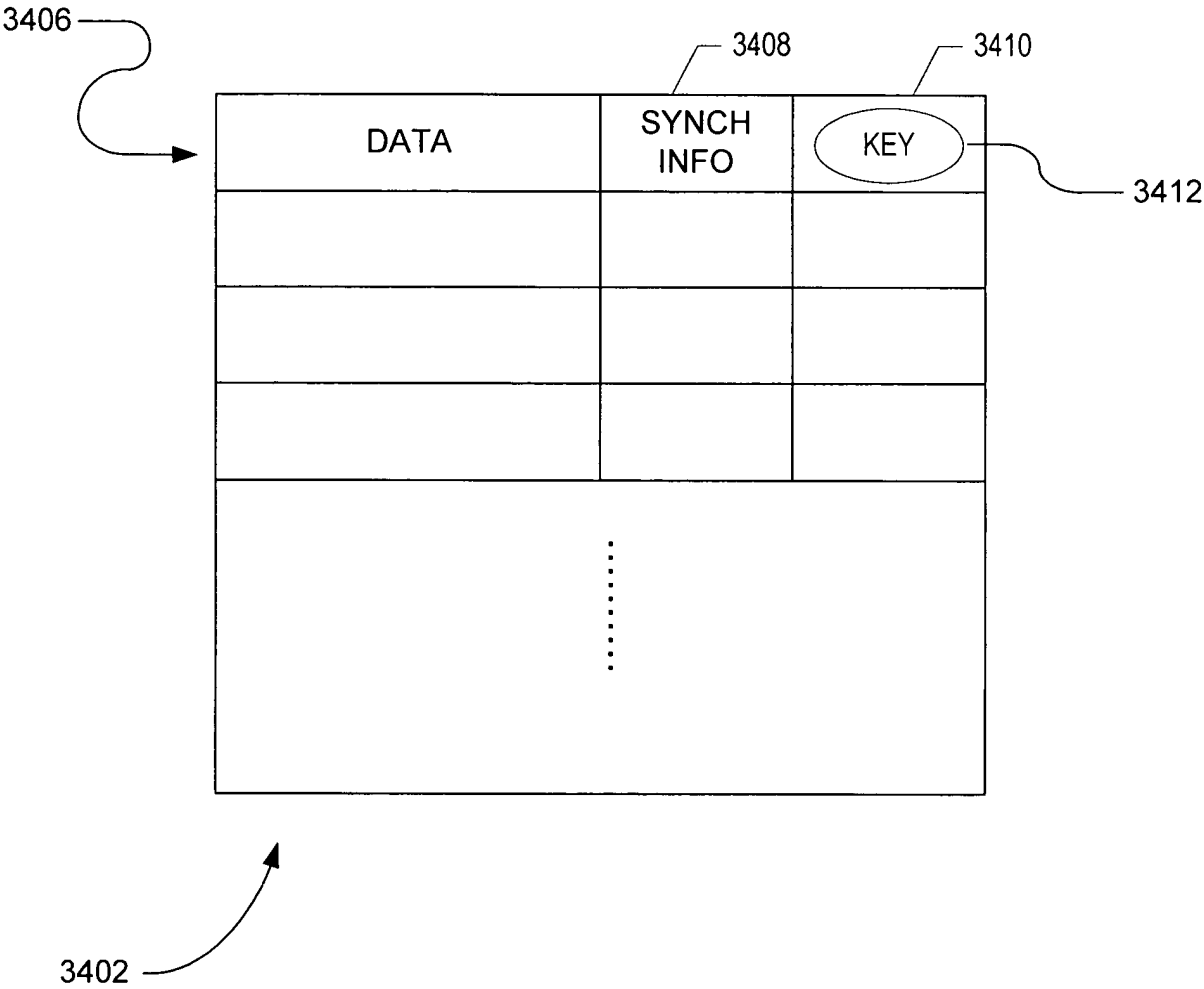


Figure 34

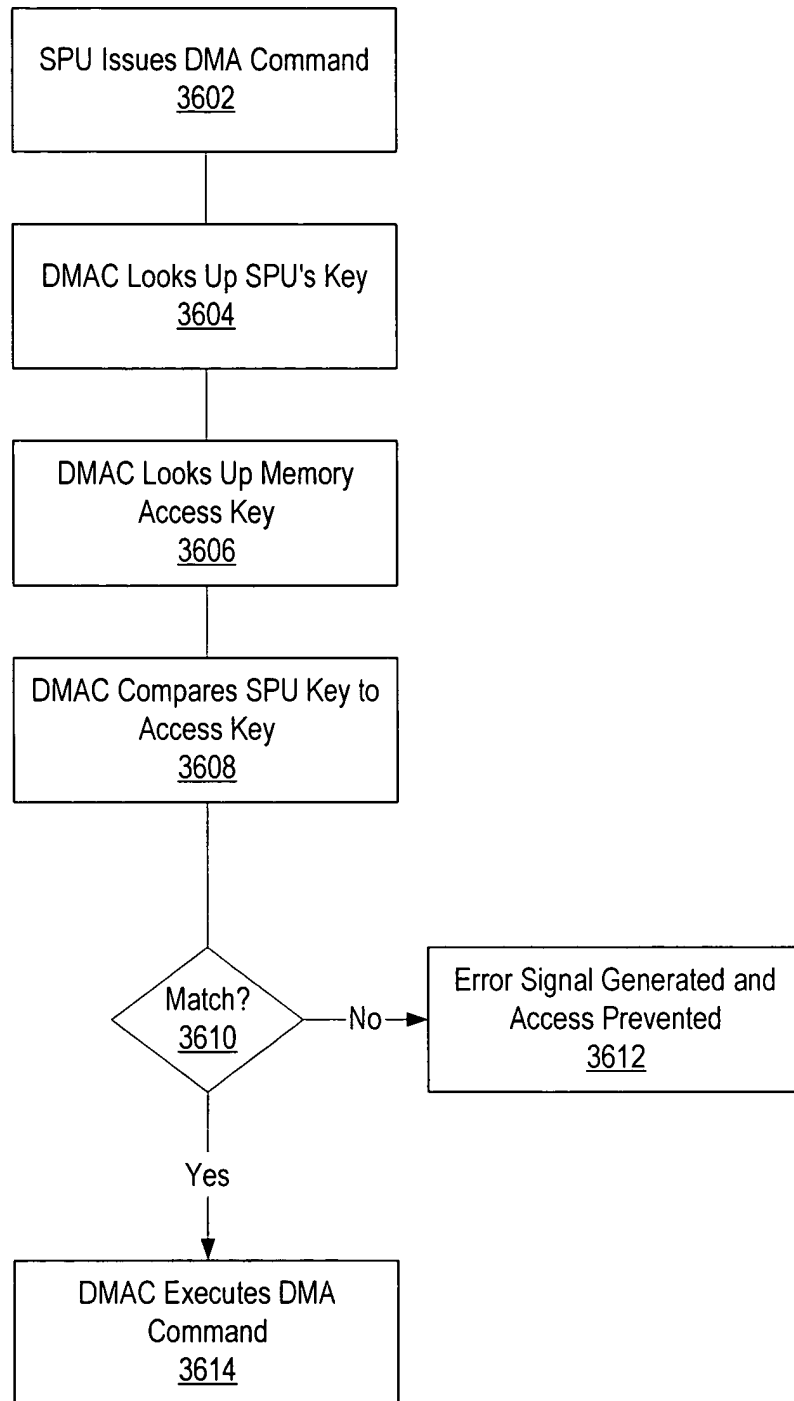
Memory Access Control Table

3504	ID	3506	3508	3510	3512
		Base	Size	Access Key	Access Key Mask
	0	Base	Size	Access Key	Access Key Mask
	1	Base	Size	Access Key	Access Key Mask
	2	Base	Size	Access Key	Access Key Mask
		⋮			
	63	Base	Size	Access Key	Access Key Mask

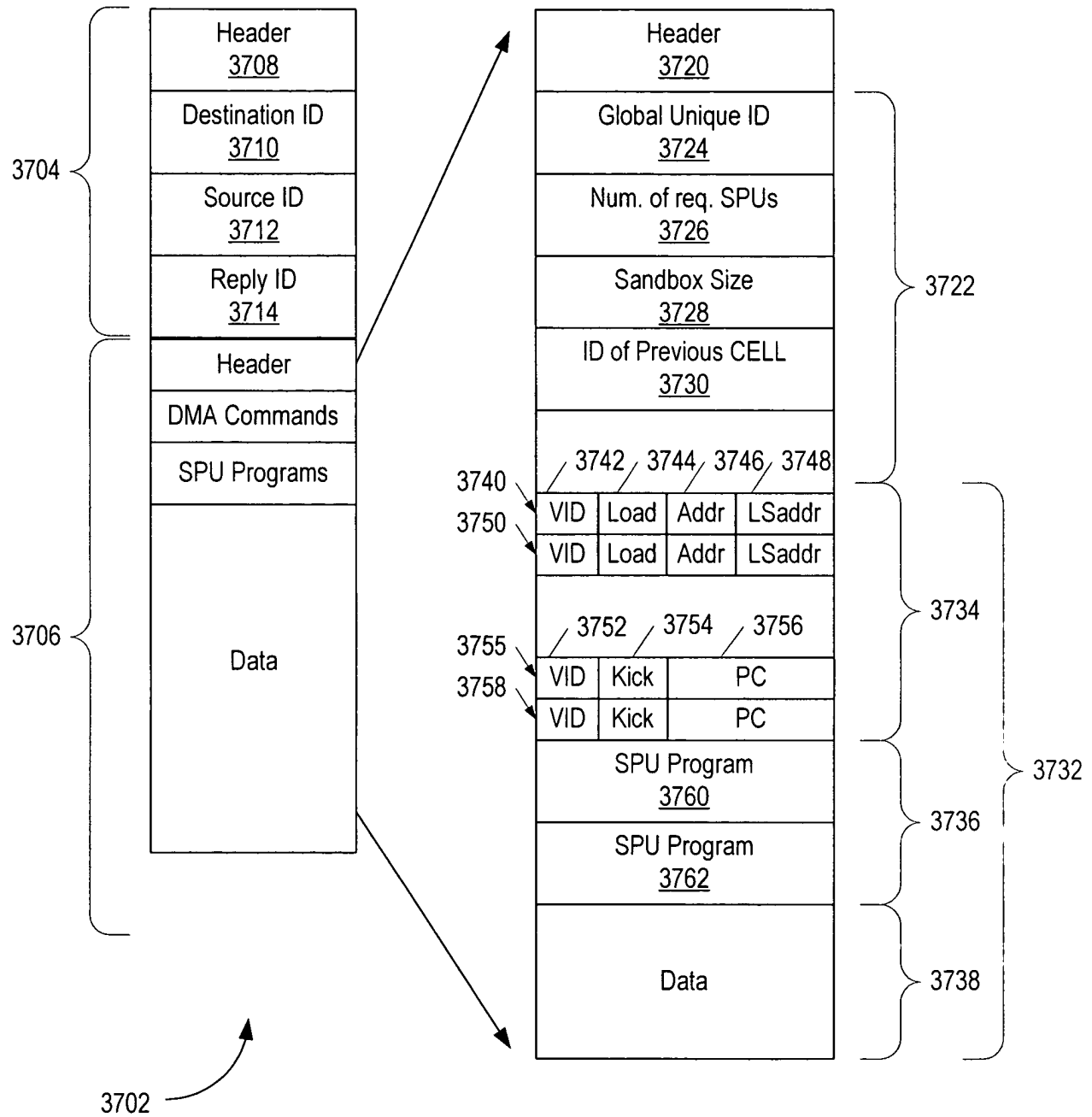
3502

Figure 35

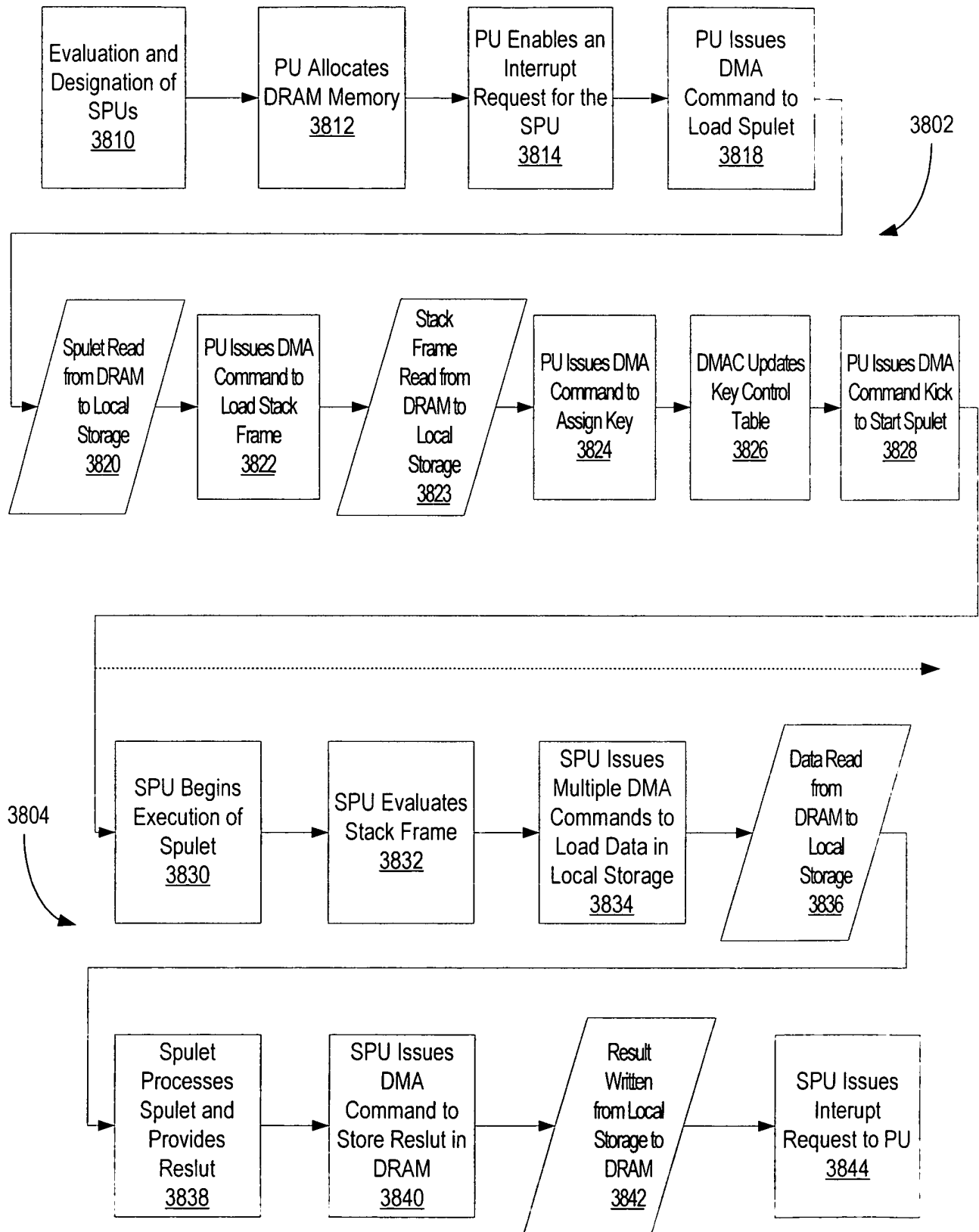
36 / 50

**Figure 36**

37 / 50

**Figure 37**

38 / 50

**Figure 38**

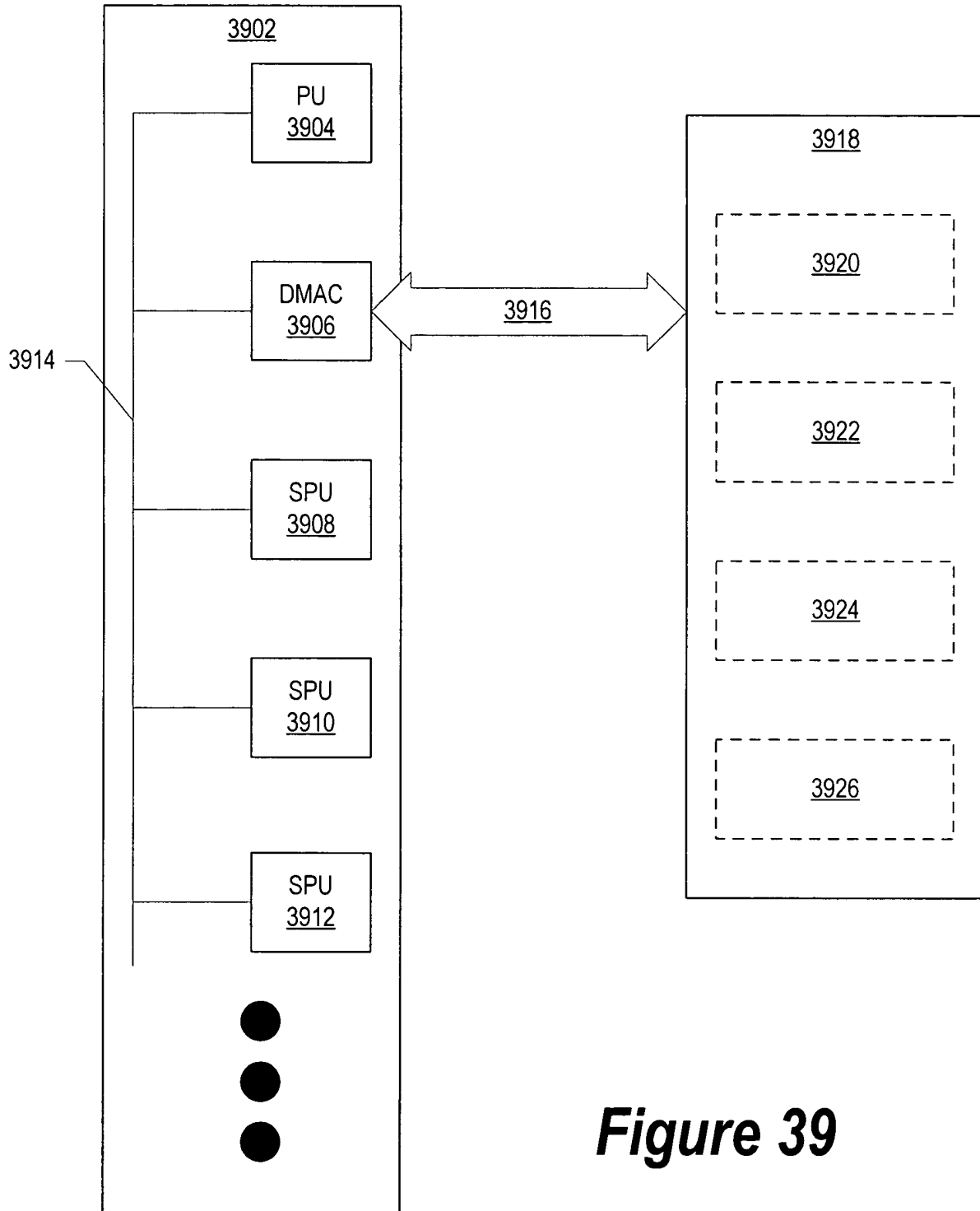
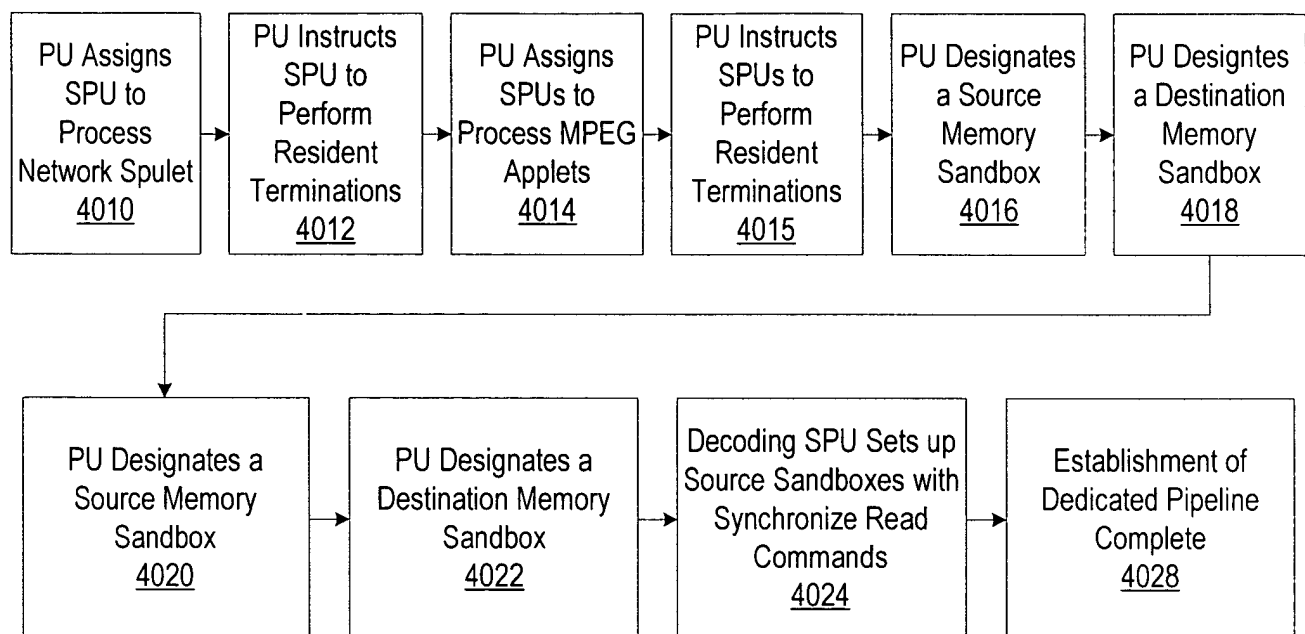
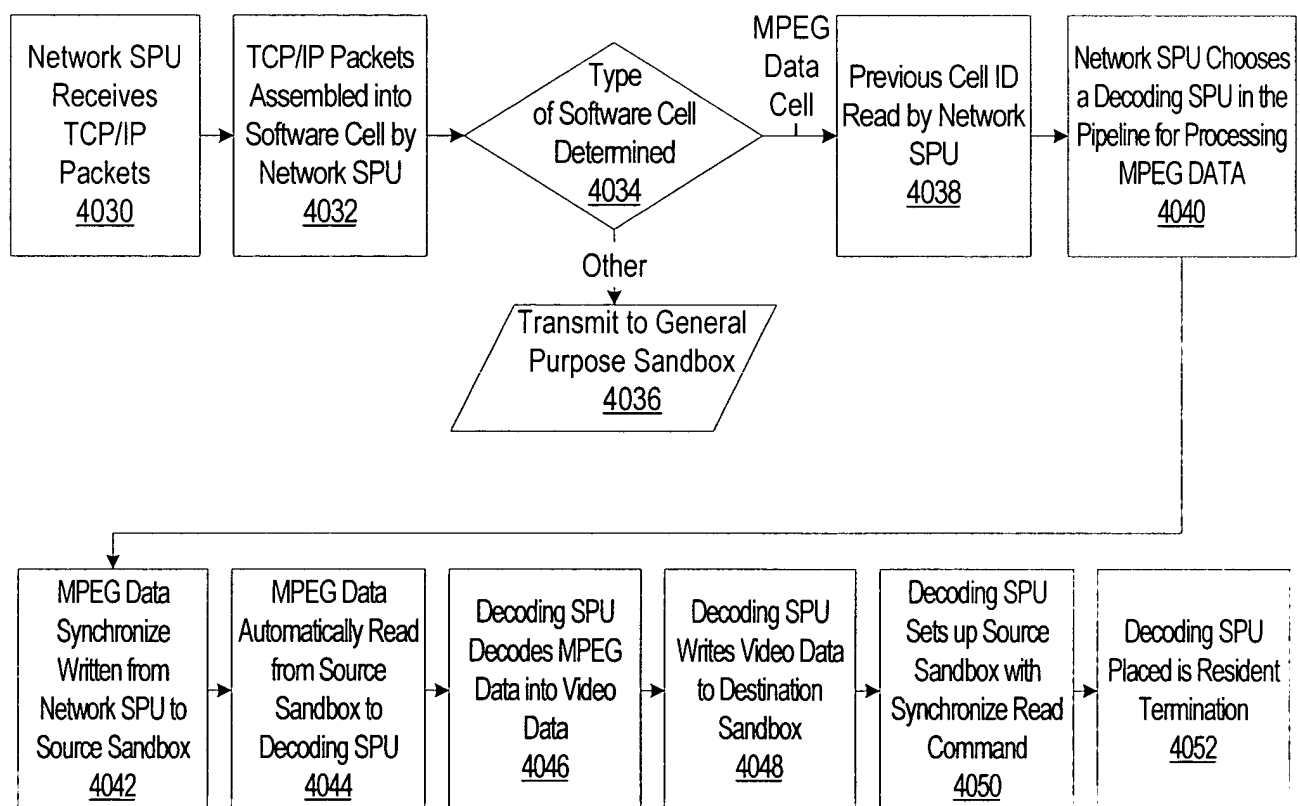
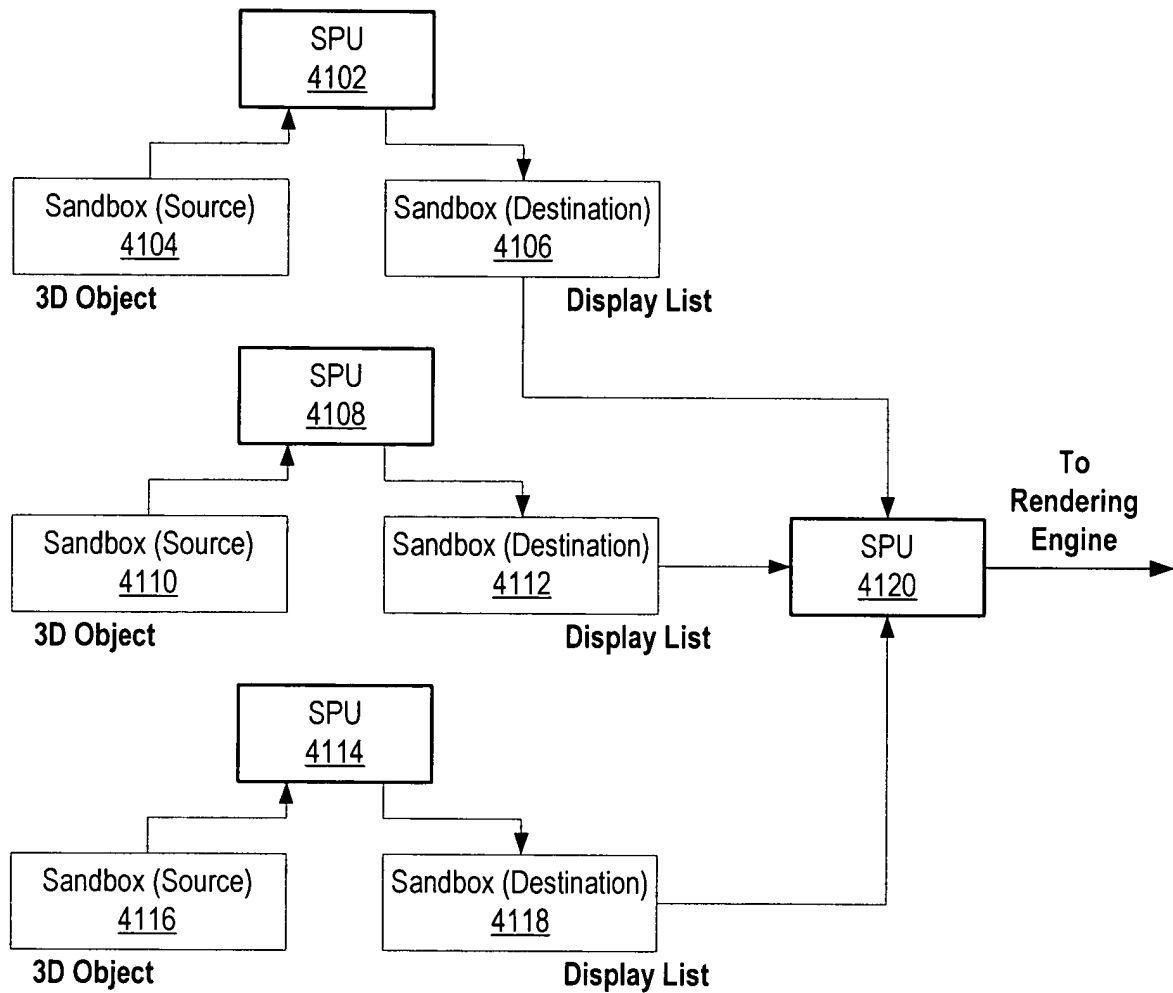


Figure 39

40 / 50

**Figure 40A****Figure 40B**

**Figure 41**

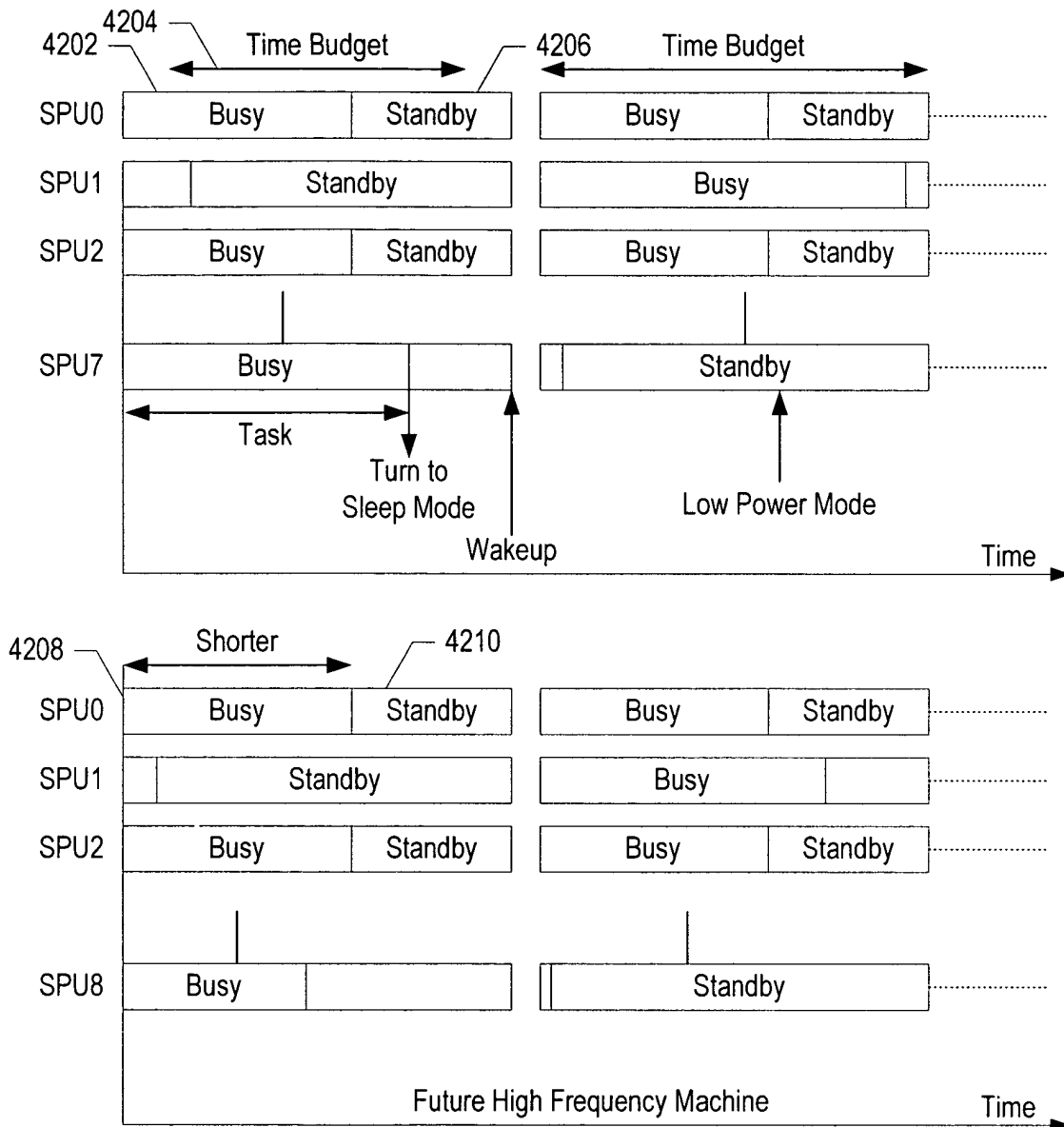
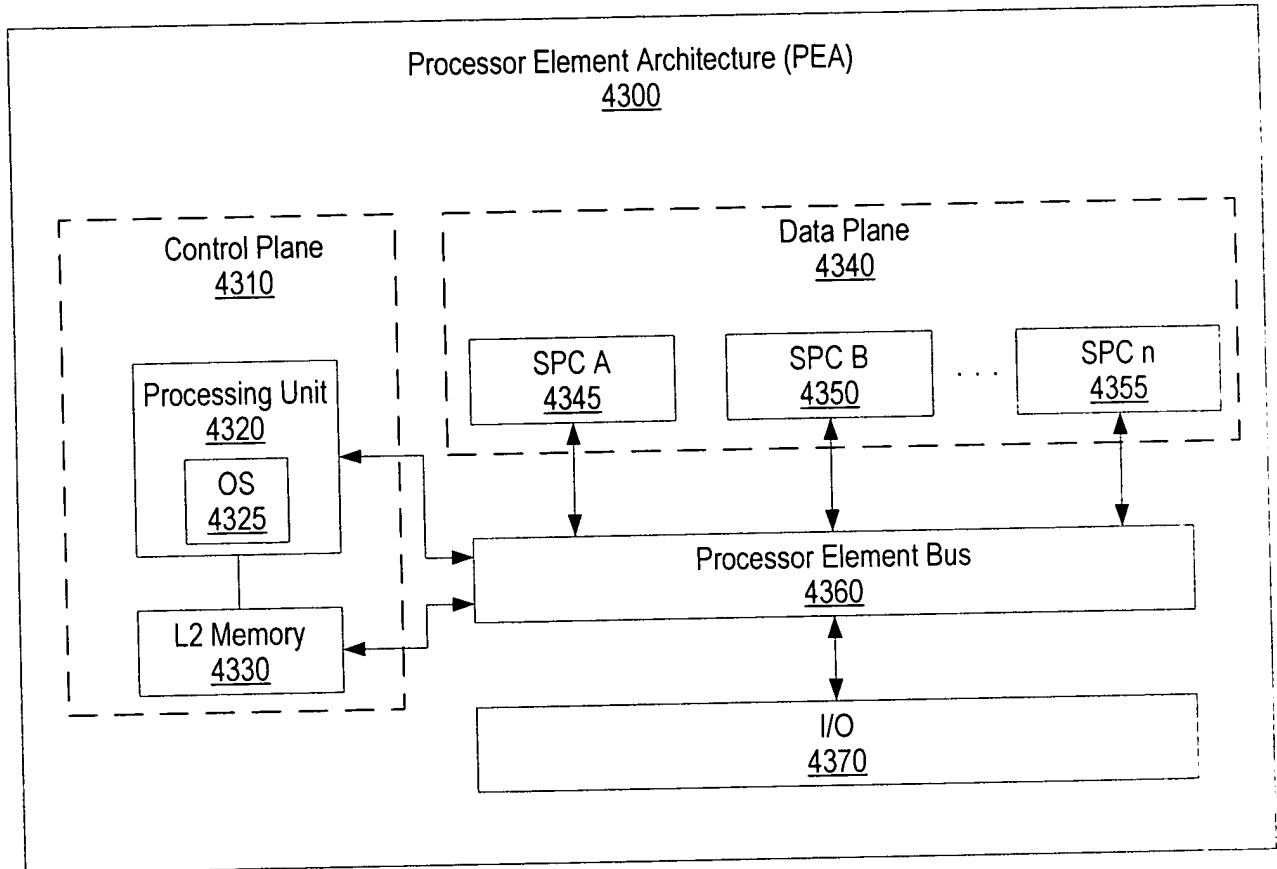
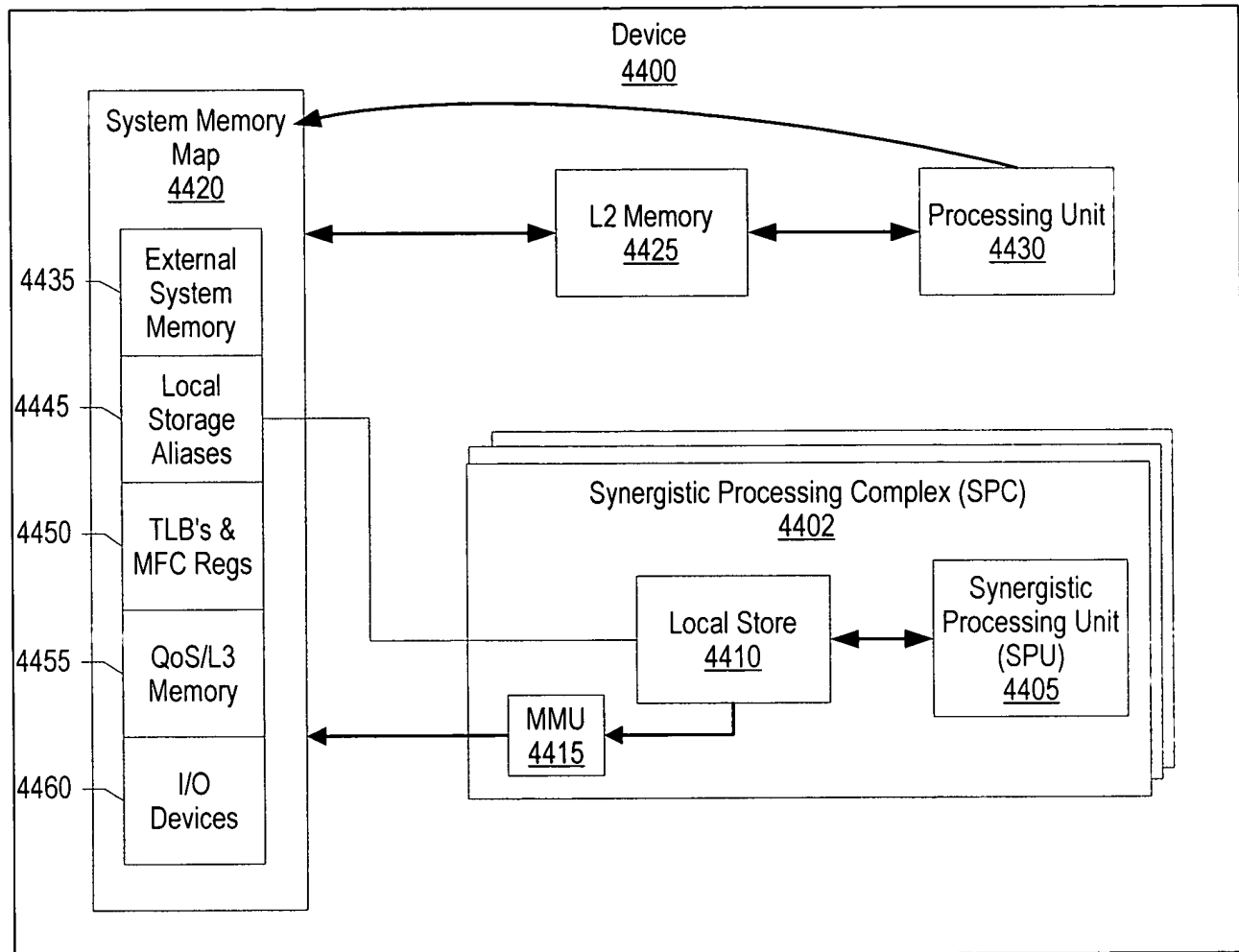
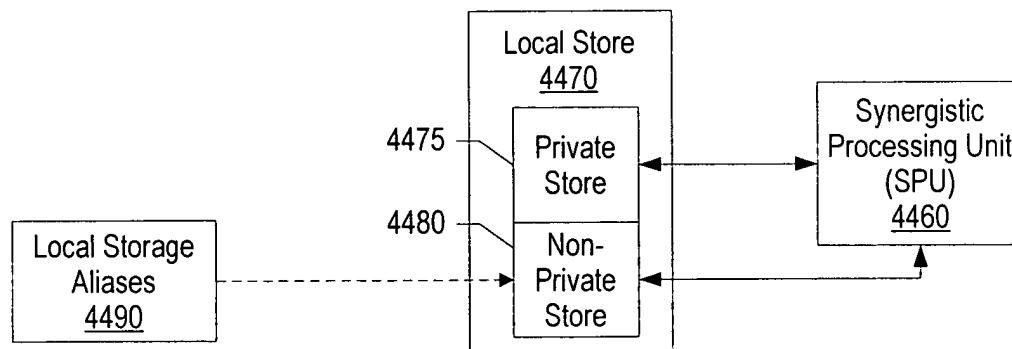


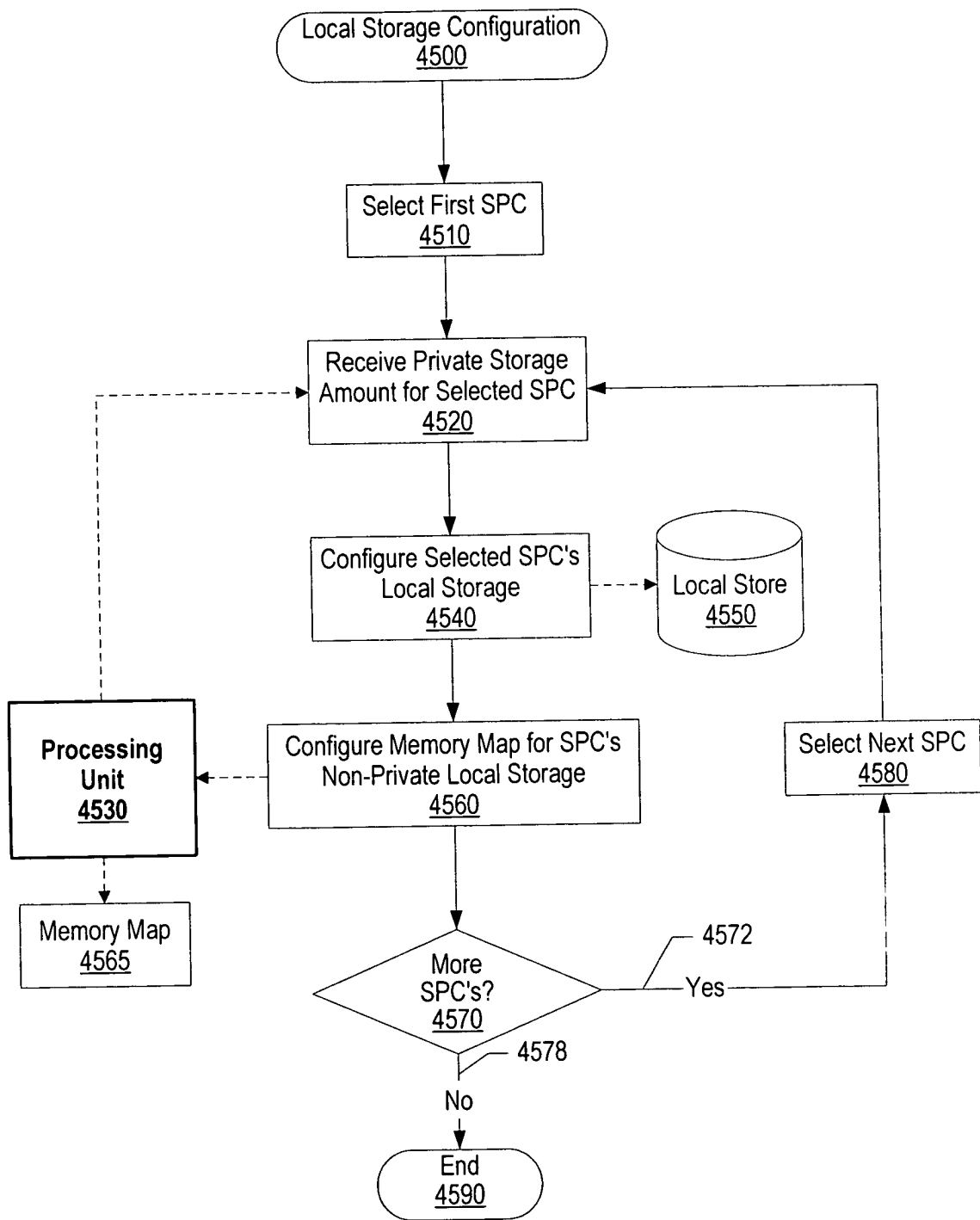
Figure 42

**Figure 43**

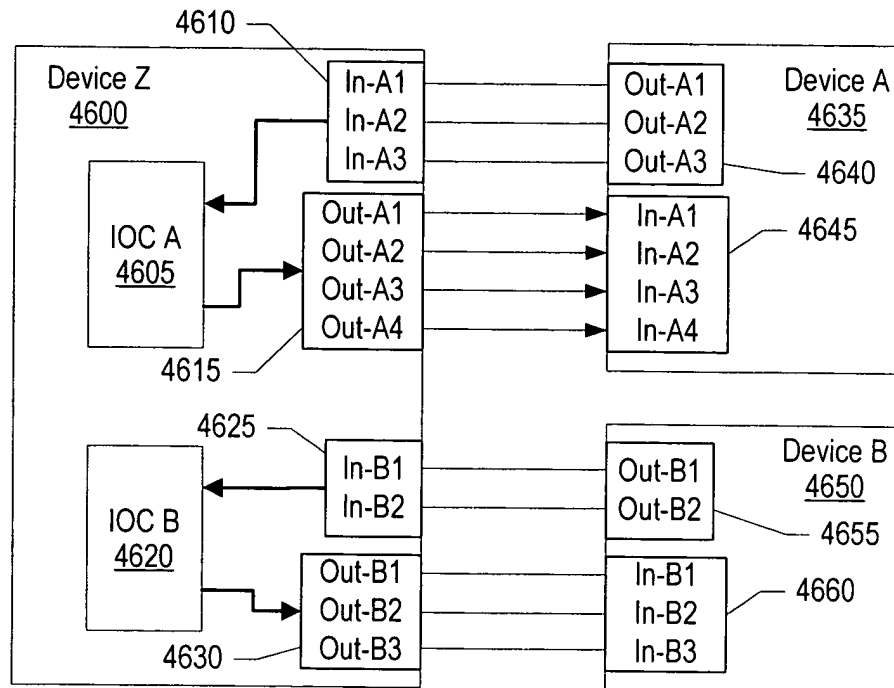
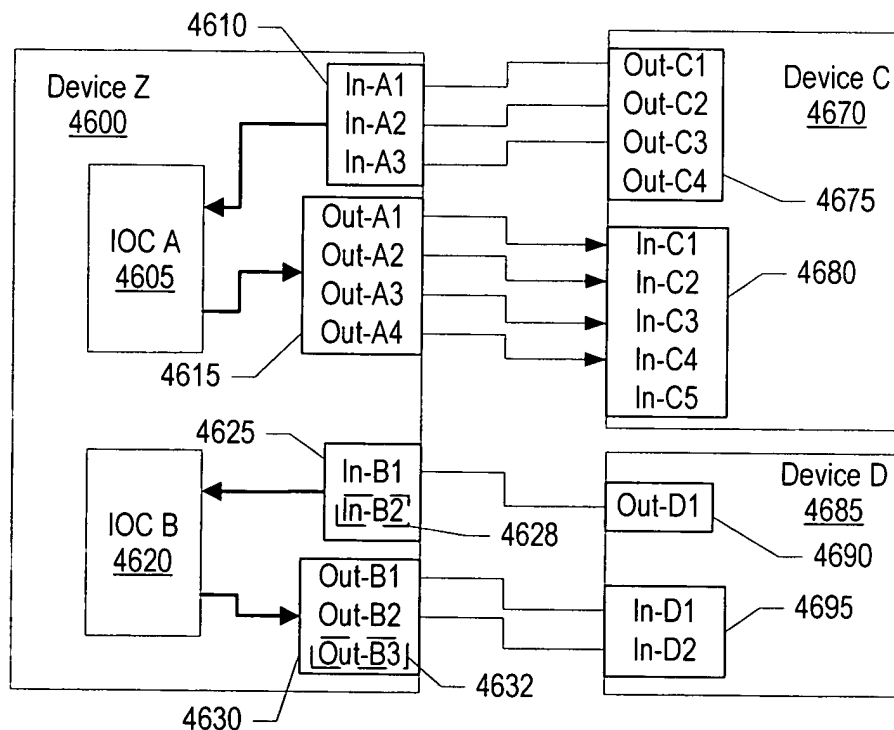
44 / 50

**Figure 44A****Figure 44B**

45 / 50

**Figure 45**

46 / 50

**Figure 46A (Prior Art)****Figure 46B (Prior Art)**

47 / 50

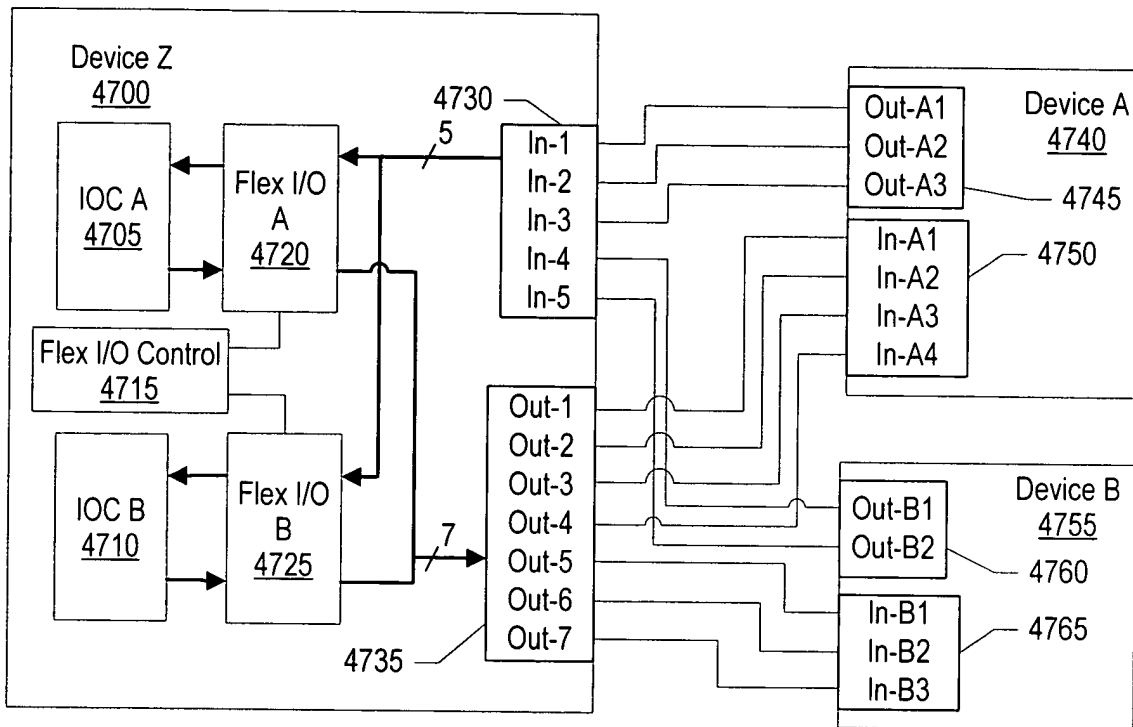


Figure 47A

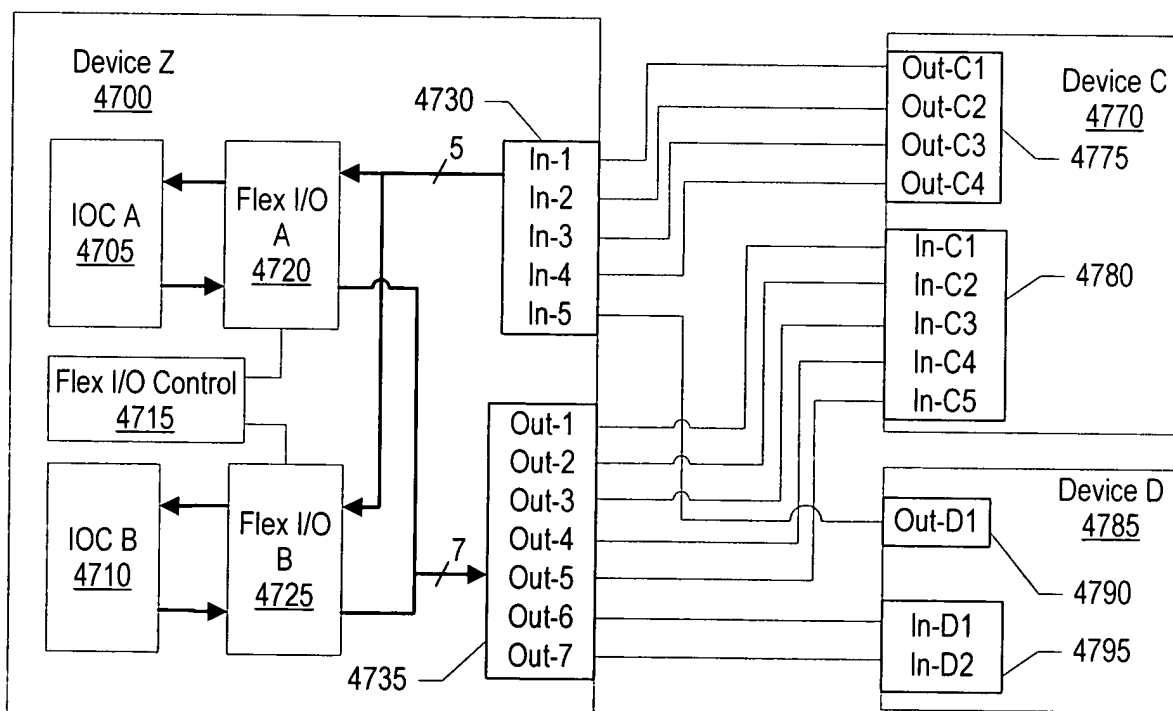


Figure 47B

48 / 50

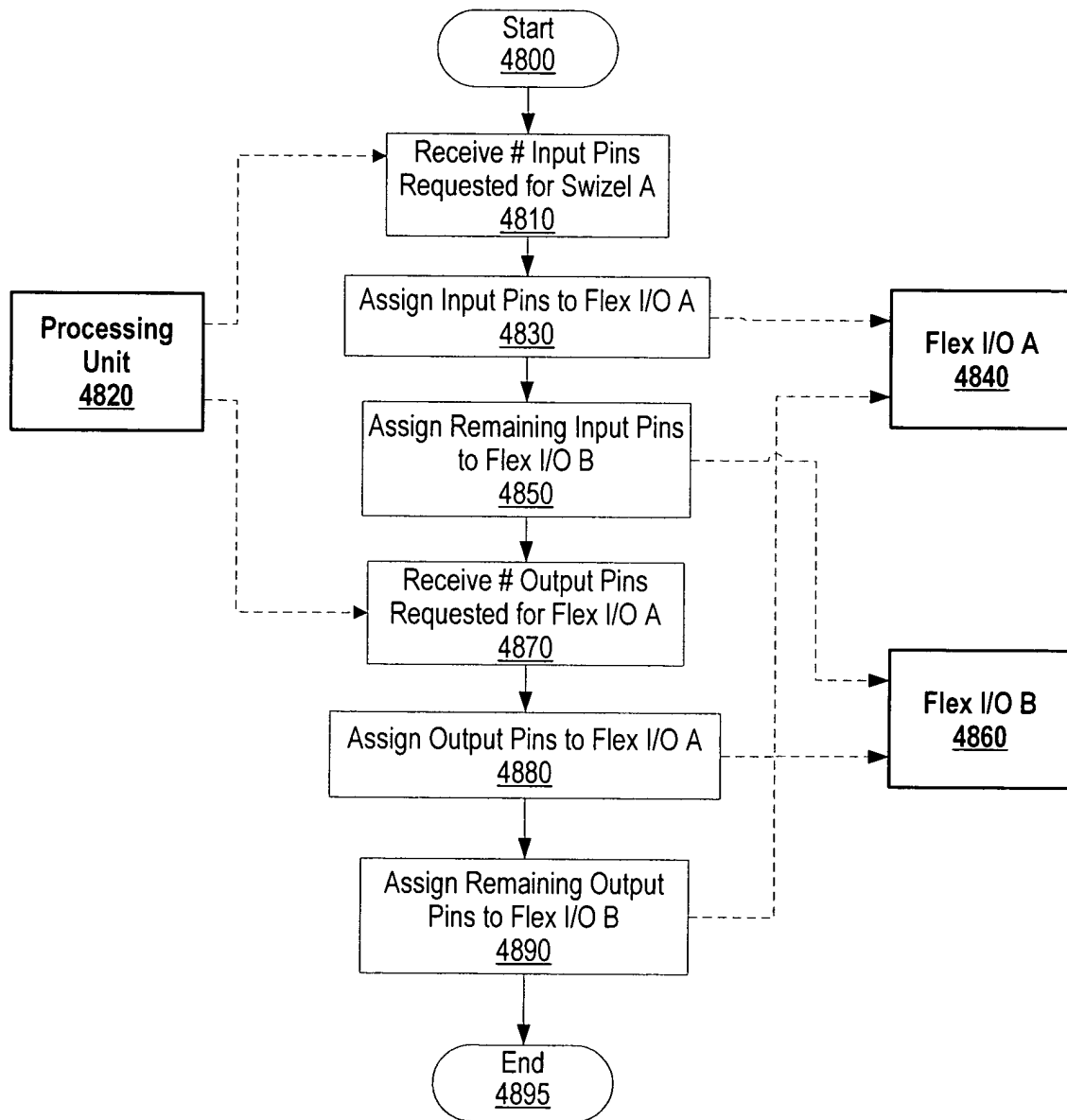
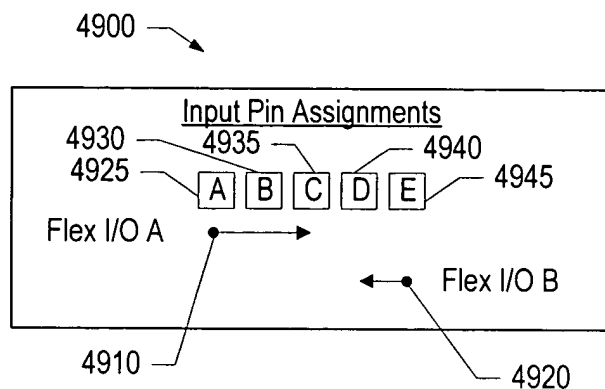
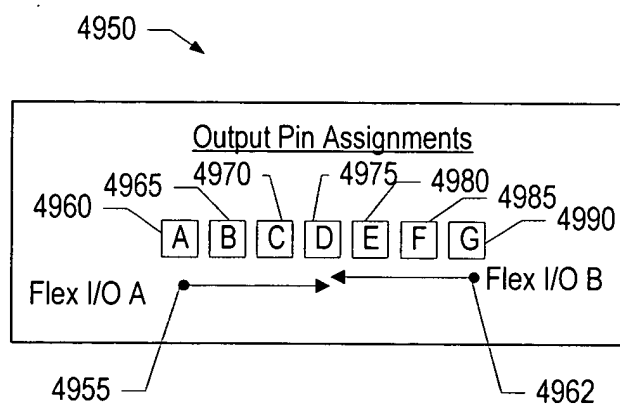
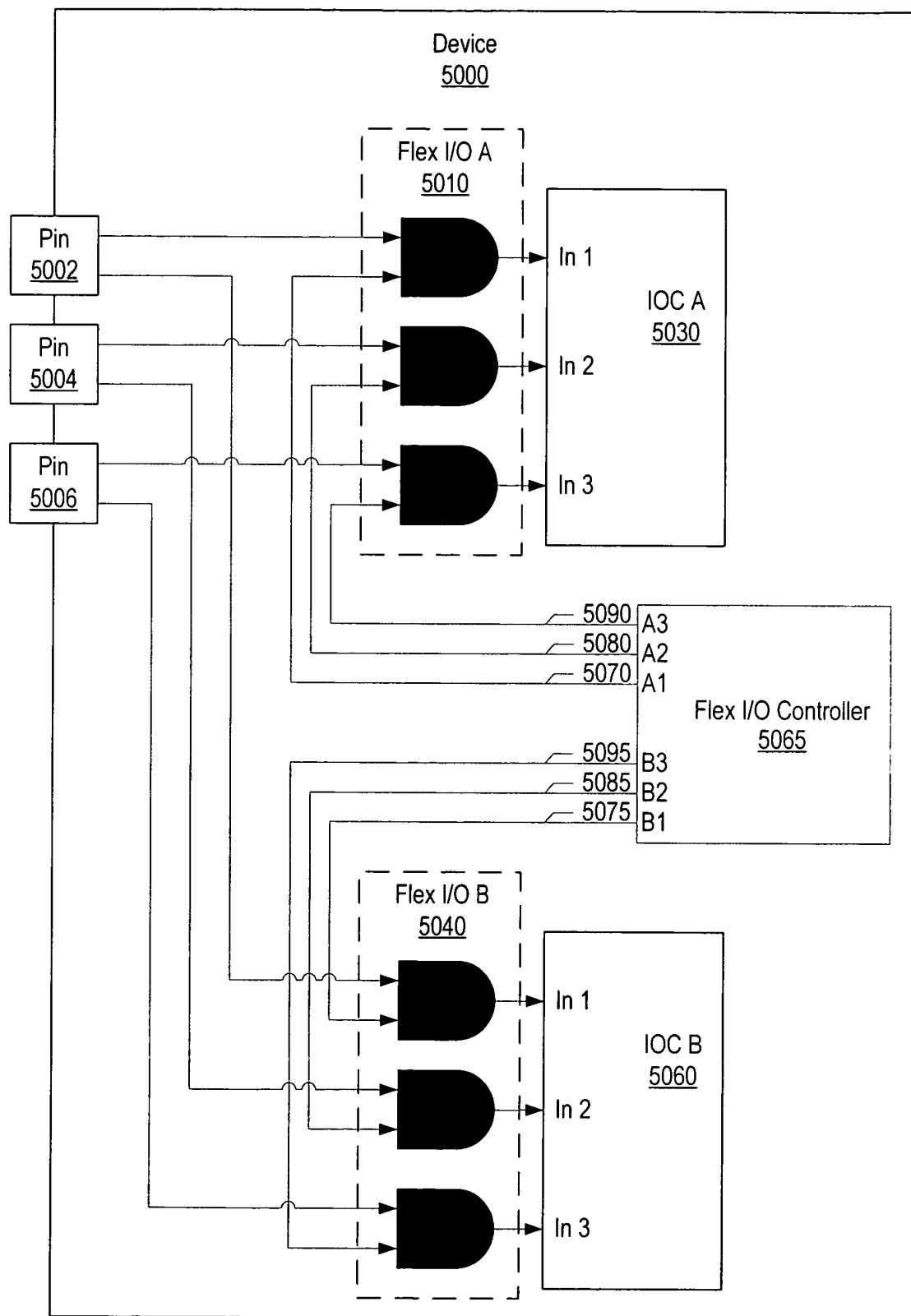


Figure 48

**Figure 49A****Figure 49B**

50 / 50

**Figure 50**